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Accelerated FDTD Analysis of Antennas Loaded by Electric Circuits

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Abstract—A fast FDTD method for the analysis of antennas loaded by non-linear electric circuits is introduced. In the present analysis, the modified nodal analysis (MNA) method is coupled with the FDTD method. The time-periodic explicit error correction (TP-EEC) method is applied to the MNA method for accelerated computation of the transient processes. The present method is applied to analysis of simplified models of an RFID tag composed of a non-linear electric circuit and line antenna. It is shown that the present method can effectively shorten the computational time by accelerating the transient processes.

Index Terms—FDTD method, TP-EEC method, Electromagnetic waves, Modified nodal analysis, RFID tag

I. INTRODUCTION

FIELD COMPUTATION METHODS, such as the finite difference time-domain (FDTD) method [1], [2] and the method of moment [3], have widely been used for analysis of high frequency electronic devices. In recent years, these methods have been applied to coupling analysis of high-frequency electromagnetic fields and electric circuits for the design of high-frequency electronic devices and analysis of electromagnetic compatibility (EMC) problems [4]. In the coupling analysis, circuit simulation involving nonlinearity requires time domain computations. For this reason, the coupling analysis of electromagnetic fields and nonlinear electric circuits usually requires high computational cost. When the time constant of the circuit is much longer than the time period of electromagnetic waves, this problem becomes quite severe because the number of time steps must be considerably large. It would be possible to reduce the computational cost if one could effectively shorten the time constant, that is, accelerate the computation of the transient processes of the circuit.

The time-periodic explicit error correction (TP-EEC) method [5], [6], which accelerates the transient processes of time-periodic systems, has been introduced for reduction of computational costs. The TP-EEC method is based on the assumption that the unknown variables are temporally periodic in the steady state and slowly converging components without periodicity can be separated from them. The slowly converging components are then determined by solving small-scale correction equations to correct the transient solutions. The TP-EEC method is an extension of the EEC method [7], which gives a general framework of the error correction based on the decomposition of unknowns to fast and slowly converging components. It has been shown that the EEC has a common theoretical basis with the deflation methods [8], which have been applied to linear systems of poor convergence [9], [10]. The TP-EEC method has been applied to finite element (FE) analysis of motors and coupling FE analysis of circuit and eddy current fields [6], in the latter of which inductance is computed in the FE analysis by taking magnetic saturation into account.

In this paper, we will discuss the effectiveness of the TP-EEC method when applied to antenna analysis, where coupling between electromagnetic waves and a non-linear circuit is considered. In particular, we consider here transient analysis of dipole antennas loaded by a non-linear circuit, which are simplified models of the UHF-band RFID tag. In the design optimization of antennas for RFID tags, the coupled problem between the electromagnetic waves and the circuit must be repeatedly solved [11]. Hence the reduction in the computational cost for the coupling analysis is of fundamental importance. Moreover, in this paper, we will introduce a theoretical basis of the TP-EEC method for explanation of the reason why it is effective for acceleration of the transient processes.

In this work, the FDTD method and Modified Nodal Analysis (MNA) [12], [13] are employed for the coupling analysis of a high frequency electromagnetic field and a non-linear circuit. This paper will be organized as follows: in Section II, the coupled method with the FDTD method and MNA will be formulated. Moreover a computational procedure of the present method will be described. In Section III, the TP-EEC method will be formulated, and effect of the TP-EEC method will be discussed, while in Section IV, numerical results will be shown to verify the present method.
II. HYBRIDIZATION OF FDTD METHOD AND MNA

The hybridization of the FDTD method and MNA will be described in the following. The Maxwell equations

\[
\begin{align*}
\frac{\varepsilon}{\partial t} \frac{\partial E}{\partial t} + \mathbf{J}(E) &= \nabla \times \mathbf{H} \quad (1-a) \\
\mu \frac{\partial H}{\partial t} &= \nabla \times \mathbf{E} \quad (1-b)
\end{align*}
\]

are considered in this paper where the conduction current density \( \mathbf{J} \) is determined from the voltage-current characteristics of the non-linear circuit. In the FDTD process, (1-a) and (1-b) are approximated by the central differences in time and space and explicitly solved in turns as follows [2]:

\[
\begin{align*}
E^n_{n+1/2} &= E^{n-1/2} + \frac{\Delta t}{\varepsilon} \nabla \times \mathbf{H}^{n-1/2} \\
H^{n+1/2} &= H^{n-1/2} + \frac{\Delta t}{\mu} \nabla \times E^n.
\end{align*}
\]

Let us consider the line antenna loaded by a non-linear circuit, shown in Fig. 1, parallel to the z-axis, where \( \Delta X, \Delta Y \) and \( \Delta Z \) are the cell size of the FDTD method. The spatial size of the non-linear circuit is assumed to be sufficiently smaller than that of the FDTD cell. By integrating (1-a) on the surface \( S \) of the FDTD cell, we obtain

\[
C_0 \frac{\partial V_L}{\partial t} + I_L(V_L) = I
\]

where \( V_L = E_Z \Delta Z \) is the voltage imposed to the circuit, \( C_0 = \varepsilon \Delta X \Delta Y / \Delta Z \) is the capacitance of the FDTD cell, \( I_L \) is the current flowing into the non-linear circuit and the \( I \) is total current given by

\[
I = \int_S \mathbf{H} \cdot d\mathbf{s}.
\]

The equivalent circuit governed by (3) is shown in Fig. 2 [12], [13]. This circuit is composed of a parallel circuit of the current source \( I \) computed from (4), the capacitance \( C_0 \) and the non-linear circuit. The equivalent circuit shown in Fig. 2 is analyzed by MNA in this work. Modified nodal analysis determines the voltages between nodes in the electrical circuit according to Kirchhoff’s Current Law. A system of the non-linear circuit equations of the form

\[
f(x) + C \frac{dx}{dt} = b(t)
\]

is solved, where \( x(t) \in \mathbb{R}^N \) is composed of the unknown nodal voltages, \( b(t) \) current source driven by the antenna, \( f \) non-linear function which includes the effects of the active devices such as diodes and transistors, and \( C \) is the capacitance matrix. Note
here that equation (5) includes (3).

Fig. 3 shows the flow diagram of the coupling analysis of the FDTD method and MNA. In the FDTD process, the magnetic field \( \mathbf{H}^{n+1/2} \) is computed, where \( n \) denotes the time step, and \( p_{n+1/2} \) is computed from \( \mathbf{H}_{p}^{n+1/2} \) by (4). Then, \( p_{1}^{n} \) is substituted to the right-hand side of (5), which is solved by MNA for \( F_{i}^{n} \). The resultant electric field \( E_{i}^{n} = V_{i}^{n}/\Delta Z \) is substituted to (2-b), which is solved by the FDTD method for \( F_{p}^{n+1/2} \). These computations of \( E, H, I \) and \( V_{i}^{n} \) are repeated until a steady state is obtained.

III. ACCELERATION OF CONVERGENCE IN CIRCUIT ANALYSIS

This section describes the TP-EEC method which accelerates the transient processes of the circuit.

A. Discretized Circuit Equation

Discretization of (5) with the finite difference leads to a system of nonlinear equations of the form

\[
C(x_{n}) + P(x_{n+1}) = \tilde{b}
\]

(6)

where

\[
C(x_{n}) = \theta f(x_{n}) + C \frac{x_{n}}{\Delta t}
\]

(7-a)

\[
P(x_{n}) = (1 - \theta) f(x_{n}) - C \frac{x_{n}}{\Delta t}
\]

(7-b)

\[
\tilde{b} = \theta b_{n} + (1 - \theta)b_{n-1}
\]

(7-c)

and \( 0 \leq \theta \leq 1 \). When \( \theta \) equals to 1, for example, this scheme corresponds to backward Euler method. In this work, (6) is solved with the Newton-Raphson method at each time step.

B. TP-EEC Method

When the time constant of the system governed by (6) is much longer than \( \Delta t \), high computational cost is required to obtain the steady state solutions which do not vary in time. To accelerate the convergence to the steady state, the TP-EEC method is applied to (6).

It is assumed that \( b_{n} \) is periodic, which satisfies \( b_{n} = b_{n+N} \). The solution \( x_{n} \) to (6) is then expected to have periodicity in the steady state. On the other hand, in the transient state, periodicity would approximately hold, that is,

\[
x_{0} \approx x_{N_{x}}.
\]

(8)

To apply the TP-EEC method to (6), we introduce the vectors defined for each period as

\[
X = \begin{bmatrix} x_{1} \\ x_{2} \\ \vdots \\ x_{N_{x}} \end{bmatrix}, \quad B = \begin{bmatrix} \tilde{b}_{1} \\ \tilde{b}_{2} \\ \vdots \\ \tilde{b}_{N_{x}} \end{bmatrix}
\]

(9)

Equation (6) for one period is now expressed in the form

\[
A(X) = B.
\]

(10)

To accelerate convergence to the steady state, \( X \) is decomposed into fast and slowly converging components as follows:

\[
X = \tilde{X} + Wp
\]

(11)

where \( p \in \mathbb{R}^{MN} \) and \( W \in \mathbb{R}^{MN \times MN} \) represent the correction vector and matrix composed of the slowly converging components whose explicit forms will be described below. The constant \( M \) represents the degree of the correction. The approximated solution \( X \) given by (12) is substituted to (11) to obtain the residual vector given by

\[
r' = B - A(\tilde{X} + Wp).
\]

(12)

We expand (13) around \( \tilde{X} \) for linearization, and then employ the Gelerkin approximation that \( r' \) in (13) is orthogonal to the column vectors in \( W \) to obtain the correction equation given by

\[
W^{T}AWp = W^{T}r
\]

(13)

where \( r = B - A(\tilde{X}) \) and

\[
A = \begin{bmatrix}
C_{1} & 0 & 0 & \cdots & 0 & P_{N_{x}} \\
C_{2} & 0 & 0 & \cdots & 0 & 0 \\
\vdots & \ddots & \ddots & \ddots & \vdots & \vdots \\
0 & \cdots & P_{N_{x}-1} & C_{N_{x}} & \cdots & 0 \\
0 & 0 & 0 & \cdots & P_{N_{x}-1} & C_{N_{x}}
\end{bmatrix}
\]

(14)

The correction matrix \( W \in \mathbb{R}^{MN \times N} \) of the 0th order (\( M=1 \)), in which the slowly converging components are assumed to be temporally constant, is given by

\[
W = [I \ 1 \ \cdots \ I]^{T}
\]

(15)

where \( I \in \mathbb{R}^{N \times N} \) is the unit matrix. By substituting (17) to the
left-hand side of (14), one has

$$W^t A W = \sum_{n=1}^{N_S} (C_n + P_n).$$

Then it is assumed that (11) can be solved with sufficiently high accuracy except at $n=0$ where imperfect periodicity would result in non-negligible residuals. Under this assumption, the residual can be expressed in the form

$$r = \begin{bmatrix} P(x_{n_f}) - p_0 \\ 0 \\ \vdots \\ 0 \end{bmatrix}.$$

Substitution of (18) and (19) to (14) yields

$$W = \begin{bmatrix} 1 & \cdots & 1 \\ \cdots & \cdots & \cdots \\ -1 & \cdots & f_n I \end{bmatrix}.$$

By solving (20) for $p$, $X$ is corrected from (12).

Convergence to the steady state can further be accelerated by use of the correction of the 1st order ($M=2$) in which the slowly converging components are assumed to be temporally constant and linear. The corresponding correction matrix is given by

$$W = \begin{bmatrix} 1 & \cdots & 1 \\ \cdots & \cdots & \cdots \\ -1 & \cdots & f_n I \end{bmatrix}.$$

where

$$f_n = \frac{2n - N_S}{N_S}, \quad (0 \leq n \leq N_S).$$

By substituting (21) to (14), one obtains

$$x_n = x^n + p_0 + f_n p_1.$$

Although increase in the degree of the correction, e.g. from 0th to 1st, is expected to give better convergence, it also results in increase in the unknowns in the correction equation.

C. Effect of TP-EEC Method

The mathematical property of the TP-EEC method has been discussed for the scalar linear diffusion equation [5]. We give here more general discussion on this method. By substituting $r$ and (14) to (12), we obtain

$$X = \bar{X} + W(W^t A W)^{-1} W^t (B - A \bar{X}).$$

The steady solution to (11) is here expressed by $X^*$. Then, the error $\bar{E} = X^* - \bar{X}$ is modified after the error correction in the form

$$E = p \bar{E}$$

where $E = X^* - X$ and $p$ is defined by

$$P = I - W(W^t A W)^{-1} W^t A$$

which is a projection matrix satisfying $P^2 = P$. It can easily be seen from (26) that $P W = 0$. The error $\bar{E}$ can be decomposed in the form

$$\bar{E} = E_{\text{slow}} + E_{\text{fast}}$$

where

$$E_{\text{slow}} \in \text{Range}(W), \quad E_{\text{fast}} \in \text{Ker}(W^t A).$$

Therein, the errors $E_{\text{slow}}$ and $E_{\text{fast}}$ represent the slowly and fast converging errors, and they satisfy A-orthogonal relation $(E_{\text{slow}}, E_{\text{fast}})^t = 0$. It can be seen from (25) to (28) that the slowly converging error is eliminated as $P E_{\text{slow}} = 0$ by the TP-EEC method. On the other hand, $E_{\text{fast}}$ has no effects from the correction because of the property $P E_{\text{fast}} = E_{\text{fast}}$. Thus this error component is reduced by the iterative solution of (11) where its convergence is expected to be fast by definition. It is known that the multigrid method, which effectively eliminates the slowly converging components with spatially smooth profiles by mapping them to coarser meshes, is also based on the above mentioned decomposition and selective elimination [14]. More detailed discussions on the TP-EEC method are can be found in [15].
IV. Numerical Result

A. CR Diode Series Circuit

The half-wave dipole antenna loaded by the non-linear circuit shown in Fig. 4 is analyzed by hybridization of the FDTD method and MNA to test acceleration of convergence to the steady state by applying the TP-EEC method to MNA. The nodal voltages of the non-linear circuit are obtained from MNA. The half-wave dipole antenna is assumed to be illuminated by the plane wave. The amplitude and frequency of incident wave is assumed to be 20 V/m and 1 GHz. The size of the FDTD cell, $\Delta X=\Delta Y=\Delta Z$, is set to 3mm. The perfect matched layer is employed to enforce the free space conditions on the domain boundary. The half-wave dipole antenna is parallel to $z$-axis and set to perfect conductor ($E_z=0$ V/m).

The nonlinear circuit is composed of a capacitance, diode and resistance as shown in Fig. 5. The capacitance $C$ is set to 10pF and the resistance $R$ is set to 1k$\Omega$. The diode circuit is assumed to obey $V$-$I$ characteristics given by

$$
i_d = \begin{cases} 
1.0 \times 10^{-8} v_d & v_d \leq 0 \\
2.9 \times 10^{-7} [\exp(15 v_d) - 1] & v_d > 0 
\end{cases}$$

(29)

where $v_d$ is the voltage at diode. For negative voltages, the diode current is nearly zero. The explicit form of the circuit equation (5) for the circuit shown in Fig. 5 is given in the Appendix. In this analysis, $\theta$ is set to 1 in MNA and $N_S$ is set to 200.

Fig. 6 shows the time evolution in the voltage $V_1$. It can be seen in Fig. 6 that the convergence to the steady state is clearly accelerated by the present method.

Fig. 7 shows the absolute error $E$ between the steady and transient solutions. The numbers of time steps required to satisfy $|E|<0.01$ for no correction, for 0th order correction, and for 1st order correction, are approximately 205000 steps (1025 ns), 2400 steps (12 ns) and 1600 steps (8 ns), respectively. This means that the TP-EEC method of the 1st order correction provides convergence to the steady state 128.1 times faster than that for non-corrected computation. Moreover, the three solutions in the steady state are found to be in good agreement.

B. Cockcroft-Walton Circuit

The TP-EEC method is now applied to the half-wave dipole antenna loaded by the CW circuit shown in Fig. 8. The explicit form of the circuit equation (5) for the circuit shown in Fig. 8 is given in the Appendix. The parameters of the FDTD method and MNA are the same as those used in subsection 4.1.

Fig. 9 shows the time evolution of the output voltage $V_{out}$ of the CW circuit. It can be seen in Fig. 9 that the convergence to the steady state is clearly accelerated by the TP-EEC method.

Fig. 10 shows the absolute error $E$ between the steady and transient solutions. The number of time steps required to satisfy $|E|<0.01$ for no correction, for 0th order correction, and for 1st
order correction, are approximately 8880 steps (44 ns), 3545 steps (18 ns) and 1800 steps (9 ns), respectively. That means that the TP-EEC method of the 1st order correction provides convergence to the steady state 4.9 times faster than that for non-corrected computation. Moreover, it is observed that the three solutions in the steady state are in good agreement. It can be seen in Fig. 7 and Fig. 10 that the effects in acceleration by the present method depend on circuits.

Moreover, it is observed that the three solutions in the steady state are in good agreement. It can be seen in Fig. 7 and Fig. 10 that the effects in acceleration by the present method depend on circuits.

V. CONCLUSIONS

In this paper, it has been shown that convergence to the steady state of a non-linear circuit driven by antenna voltage, which was analyzed by FDTD and MNA, is effectively accelerated by using the present method. The theoretical reason why the present method can improve convergence to the steady state has been discussed. To test the present method, it has been applied to analysis of a half-wave dipole antenna loaded by non-linear circuits including diodes. It has been numerically shown that the TP-EEC method effectively accelerates convergence to the steady state.

APPENDIX

This appendix describes the nodal equations for the circuit discussed in this paper. The nodal equations for the CR diode series circuit shown in Fig. 5 are given by

\[
- C_0 \frac{dV_1}{dt} + C \frac{d}{dt} (V_1 - V_L) = I \quad (A1)
\]

\[
C \frac{d}{dt} (V_L - V_1) - i_d (V_2 - V_1) = 0 \quad (A2)
\]

\[
i_d (V_1 - V_2) - \frac{V_2}{R} = 0 \quad (A3)
\]

where \(I\) is the current input obtained by the FDTD computation and \(C_0\) is the capacitance for the FDTD cell.

The nodal equations for the CW circuit shown in Fig. 8 are given by

\[
- C_0 \frac{dV_1}{dt} + C \frac{d}{dt} (V_1 - V_L) + C \frac{d}{dt} (V_3 - V_L) = I \quad (A4)
\]

\[
C \frac{d}{dt} (V_L - V_1) + i_d (V_1 - V_2) - i_d (V_1 - V_2) = 0 \quad (A5)
\]

\[
C \frac{d}{dt} V_2 + i_d (V_1 - V_2) - i_d (V_1 - V_2) = 0 \quad (A6)
\]

\[
C \frac{d}{dt} (V_2 - V_3) - i_d (V_2 - V_3) - i_d (V_2 - V_3) = 0 \quad (A7)
\]

\[
- C \frac{d}{dt} V_3 + i_d (V_2 - V_3) - \frac{V_3}{R} = 0 \quad (A8)
\]

REFERENCES


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