<table>
<thead>
<tr>
<th>Title</th>
<th>Studies on atomic layer deposition Al2O3/In0.53Ga0.47As interface formation mechanism based on air-gap capacitance-voltage method</th>
</tr>
</thead>
<tbody>
<tr>
<td>Author(s)</td>
<td>Yoshida, Toshiyuki; Hashizume, Tamotsu</td>
</tr>
<tr>
<td>Citation</td>
<td>Applied Physics Letters, 101(12): 122102</td>
</tr>
<tr>
<td>Issue Date</td>
<td>2012-09-17</td>
</tr>
<tr>
<td>Doc URL</td>
<td><a href="http://hdl.handle.net/2115/50404">http://hdl.handle.net/2115/50404</a></td>
</tr>
<tr>
<td>Rights</td>
<td>Copyright 2012 American Institute of Physics. This article may be downloaded for personal use only. Any other use requires prior permission of the author and the American Institute of Physics. The following article appeared in Appl. Phys. Lett. 101, 122102 (2012) and may be found at <a href="http://link.aip.org/link/?apl/101/122102">http://link.aip.org/link/?apl/101/122102</a></td>
</tr>
<tr>
<td>Type</td>
<td>article</td>
</tr>
<tr>
<td>File Information</td>
<td>APL101-12_122102.pdf</td>
</tr>
<tr>
<td>Hokkaido University Collection of Scholarly and Academic Papers : HUSCAP</td>
<td></td>
</tr>
</tbody>
</table>
Studies on atomic layer deposition Al2O3/In0.53Ga0.47As interface formation mechanism based on air-gap capacitance-voltage method

Toshiyuki Yoshida and Tamotsu Hashizume

Citation: Appl. Phys. Lett. 101, 122102 (2012); doi: 10.1063/1.4753927
View online: http://dx.doi.org/10.1063/1.4753927
View Table of Contents: http://apl.aip.org/resource/1/APPLAB/v101/i12
Published by the American Institute of Physics.

Related Articles
Dielectric strength, optical absorption, and deep ultraviolet detectors of hexagonal boron nitride epilayers

Scaling of equivalent oxide thickness of atomic layer deposited HfO2 film using RuO2 electrodes suppressing the dielectric dead-layer effect

Critical flux ratio of hydrogen radical to film precursor in microcrystalline silicon deposition for solar cells

A facile approach for shape selective synthesis of rhodium nanostructures and conductivity studies
AIP Advances 2, 042122 (2012)

Effective passivation of In0.2Ga0.8As by HfO2 surpassing Al2O3 via in-situ atomic layer deposition

Additional information on Appl. Phys. Lett.
Journal Homepage: http://apl.aip.org/
Journal Information: http://apl.aip.org/about/about_the_journal
Top downloads: http://apl.aip.org/features/most_downloaded
Information for Authors: http://apl.aip.org/authors
Studies on atomic layer deposition Al$_2$O$_3$/In$_{0.53}$Ga$_{0.47}$As interface formation mechanism based on air-gap capacitance-voltage method

Toshiyuki Yoshida$^1$ and Tamotsu Hashizume$^2$

$^1$Interdisciplinary Faculty of Science and Engineering, Shimane University, Matsue 690-8504, Japan
$^2$Research Center for Integrated Quantum Electronics, Hokkaido University, Sapporo 060-8628, Japan

(Received 11 May 2012; accepted 4 September 2012; published online 18 September 2012)

The air-gap capacitance-voltage characteristics of InGaAs surfaces were measured after 1-, 2-, 6-, 9-, and 17-cycle atomic layer deposition (ALD) Al$_2$O$_3$ processing. A high density of mid-gap states was found to be generated and increased during these ALD process steps, while the native oxide component was reduced. On the other hand, the mid-gap state density was drastically reduced after the usual annealing process. The generation of the mid-gap states seemed to be relevant to a non-stoichiometric Al-oxide component associated with a deficit in oxygen atoms, which became re-oxidized during the annealing process. © 2012 American Institute of Physics. [http://dx.doi.org/10.1063/1.4753927]

Control of the III-V/high-k interface is a critical issue for the realization and stable operation of high-electron-mobility metal-insulator-semiconductor field-effect transistors (MISFETs). Recent progress in the atomic layer deposition (ALD) technique has allowed for the formation of high-k films on III-V materials with an abrupt and high quality interface. However, a high density of interface states (D$_i$) still exists, which causes problems such as Fermi level pinning, large hysteresis, instability, mobility deterioration, and an increase in threshold voltage. The most attractive method to reduce D$_i$ is to remove the native oxide present on the semiconductor surfaces. Various chemical treatments, including HF-, NH$_4$(OH)-, and (NH$_4$)$_2$S- treatments, have been examined to form a clean surface with less native oxide, with the intent of forming a well behaved interface. Native-oxide-free processes have also been reported, by introducing an arsenic capping layer to prevent surface oxidation and using an ultrahigh vacuum (UHV) process to examine the effect of the surface reconstruction and cation- and anion-rich structures. These studies did improve the interface quality. However, the generation mechanism of the interface states is not yet known, since the direct characterization of the effects of a native oxide in generating interface states is difficult to determine. Thus, it is necessary to understand and control the III-V/high-k interface in order to characterize the variation in D$_i$ in the initial ALD stage.

In this study, a unique air-gap capacitance-voltage (air-gap C-V) technique was used to determine the D$_i$ distribution in the initial ALD stage of Al$_2$O$_3$ on an InGaAs surface, along with an x-ray photoelectron spectroscopy (XPS) analysis. In the air-gap C-V technique, C-V measurements are carried out using a field electrode separated from the sample surface by a thin air-gap (∼300 nm), as shown in Fig. 1, providing a conventional metal-insulator-semiconductor (MIS) assessment of “insulator-free” or “ultrathin-insulator-covered” surfaces without current leakage or damage due to the metal deposition processes. The authors have previously carried out analyses of the passivation characteristics of Si,$^{23-27}$ GaAs,$^{28}$ and InP$^{29-32}$ surfaces using the air-gap C-V method. It is clear that this method can contribute to the investigation of the interface formation mechanism of Al$_2$O$_3$/InGaAs interfaces by conducting a layer-by-layer analysis in the initial stage of the ALD process.

The samples used in this study were n-In$_{0.53}$Ga$_{0.47}$As epitaxial layers with a thickness of about 3 μm and a doping density of $1 \times 10^{17}$ cm$^{-3}$, grown on n$^+$-InP ($>10^{18}$ cm$^{-3}$) substrates. The samples were placed in air for a few days, causing them to be covered with a native oxide (referred to as the “as-received” surface). The ALD process was carried out using the “as-received” surfaces (i.e., without any surface treatment using chemical or dry processes) to determine the interface formation mechanism involving the residual native surface oxide. For the ALD-Al$_2$O$_3$ process, trimethylaluminum (TMA) and H$_2$O were used as the Al and O precursors, respectively. The substrate temperature was set to 300 °C. Pure nitrogen was used as the purging and carrying gas. In this study, 1-cycle included 0.015 s H$_2$O, 7 s purging, 0.015 s TMA, and 7 s purging, corresponding to a 0.11-nm Al$_2$O$_3$ deposition.

Figure 2(a) shows the air-gap C-V characteristics for n-In$_{0.53}$Ga$_{0.47}$As surfaces after 1-, 2-, 6-, 9-, and 17-cycle ALD Al$_2$O$_3$ processes. The measurement frequency and sweep rate were 500 kHz and 1.0 V/s, respectively. The dashed line shows the calculated ideal C-V curve in the absence of interface traps. Since it is difficult to obtain an identical air-gap thickness in each measurement, there is some variation. In this figure, the range was from 245 to 305 nm. Thus, each measured curve has a corresponding ideal curve. However, as reference, only one ideal curve is shown for a bare surface.

![FIG. 1. Schematic of air-gap C-V method and its ability to measure C-V characteristics during surface/interface formation process.](http://apl.aip.org/about/rights_and_permissions)
with a 256.0-nm air-gap with $N_D = 1 \times 10^{15}$ cm$^{-3}$. Additionally, since the capacitance value is mainly determined by the capacitance of the air-gap, the effect of the thickness variations of the Al$_2$O$_3$ is extremely small, within a few nanometers, as seen in this figure. Thus, the difference between the ideal C-V curve for the bare surface and the surface after the 17-cycle ALD process (covered with about a 2-nm Al$_2$O$_3$ layer) is quite small.

In Fig. 2(a), one notable feature is that the capacitance variation range for the measured curve after a 1-cycle ALD process is comparable to that for the ideal curve, indicating the presence of a low density of fast interface states. It is estimated that the InGaAs surface was covered with a large amount of residual native oxide after only one cycle of the ALD process even if a self-cleaning effect occurred. Generally, an interface between a III-V semiconductor and a native oxide is considered to lead to a high interface state density, causing strong Fermi level pinning and less capacitance variation. A notable possibility is shown when using the air-gap C-V technique, in that the density of the fast interface states is unexpectedly low at the InGaAs/native-oxide interface. However, there is a large hysteresis in the measured C-V curve (not shown here) suggesting the existence of a high density of slow states. Further investigation is required.

With the increasing number of ALD process cycles, the minimum capacitance value increases and is increasingly different to the ideal one. C-V curve fittings using the one-dimensional Poisson’s equation including fast interface states were performed. The resulting $D_n$ distributions after each ALD cycle are shown in Fig. 2(b). The $D_n$ distribution after 170 cycles (about 20-nm Al$_2$O$_3$) is also shown. It is widely accepted that the surface state density should be reduced within a few ALD cycles because of the self-cleaning effect. However, from a layer-by-layer characterization using the air-gap C-V technique, the generation of mid-gap states was clearly observed in the initial few ALD cycles. In the following, a possible reason for mid-gap state generation is discussed.

Figure 3 shows the XPS As 3$d$ spectra for (a) the as-received surface, and (b) the surfaces after 4 and (c) 17 ALD cycles. The escape angle was $15^\circ$ and the intensity was normalized by that of the bulk signals. From Figs. 3(a) and 3(c), a reduction of the native oxide (self-cleaning effect) is observed, and a small amount of oxide was seen on the surface after the 17-cycle ALD process, indicating that the native oxide was removed mainly within the initial 17 cycles. From Fig. 3(a), the dominant arsenic oxide component in the native oxide was As$_2$O$_5$, and after the 4-cycle ALD process (Fig. 3(b)), some of the As$_2$O$_5$ was converted to As$_2$O$_3$ by the strong deoxidization effect of aluminum. The self-cleaning effect is usually explained in terms of the chemical reaction between the native oxide and TMA. Recent computational results on the thermodynamic Gibbs free energy indicate that Al$_2$O$_3$ can be formed by As$_2$O$_3$ and TMA producing reaction products such as As$_4$, As(CH$_3$)$_3$, C-H groups, H$_2$O, and CO$_2$.

A notable point is that oxygen-related reaction products are predicted to be produced, which are H$_2$O and CO$_2$ in this case, meaning that oxygen atoms in the native oxide are used up to form not only Al oxide but also H$_2$O and/or CO$_2$. This results in an oxygen atoms deficiency in the Al oxide (i.e., AlO$_x$, $x < 1.5$) formed in the initial stage of the ALD
process. Recent simulations \(^{18,36}\) for the Al\(_2\)O\(_3\)/InGaAs interface based on the density functional theory indicate the possibility of obtaining an interface without mid-gap states. However, in the actual ALD process, the oxygen atoms deficiency yields a non-stoichiometric Al-oxide component. Also, there is general process damage forming dangling bonds and/or strained bonding states, \(^{36}\) which are the possible origins of \(D_\text{it}\), as shown in Fig. 2(b).

Another notable point in Fig. 2(b) is the rapid increase in initial two ALD cycles. From this figure, the \(D_\text{it}\) after 2 ALD cycles became more than twice that after a one ALD cycle. The \(\text{As}^{5+}\) and \(\text{As}^{3+}\) component ratios as a function of Al\(_2\)O\(_3\) ALD cycle number were plotted in Fig. 3(d) in order to consider the possible reasons for this. The \(\text{As}^{5+}\) and \(\text{As}^{3+}\) ratios become nearly equal after two ALD Al\(_2\)O\(_3\) process cycles. This also indicates that TMA reacted with the native As-oxide in the initial ALD stage and that such an initial reaction impedes the formation of a stoichiometric Al\(_2\)O\(_3\) layer. In the actual ALD process, it is unlikely that a monolayer Al\(_2\)O\(_3\) would perfectly cover the InGaAs surface during the first ALD cycle, owing to the non-uniform distributions of the native oxide and the chemical residues on the InGaAs surface. This is a possible reason for the change in the oxide component ratio and the generation of high-density interface states after two Al\(_2\)O\(_3\) ALD cycles.

Figure 4 shows the conventional MIS C-V curves for a Ni/Al\(_2\)O\(_3\) (20 nm)/InGaAs structure (a) before and (b) after the post metallization annealing (PMA) process. The annealing was carried out at 400 °C for 15 min in pure nitrogen gas. The measurement frequency was varied from 500 Hz to 500 kHz and the sweep rate was 0.1 V/s. The dashed lines are the calculated ideal C-V curves obtained by solving the gap C-V technique. For the first 17 ALD cycles, the density of mid-gap states increased with the cycle number, and this was related to the removal of the native oxide component due to the reaction with the TMA. In this region, an

\[ D_\text{it} \]

...
oxygen deficiency should occur, resulting in the formation of a non-stoichiometric Al-oxide component. This mid-gap state remained after 170 ALD cycles (20-nm Al₂O₃). However, after the usual PMA process, these mid-gap states were drastically reduced and the C-V characteristics became normal, even if the surface was not subjected to a chemical treatment before the ALD process. Re-oxidation of the non-stoichiometric Al-oxide using the oxygen from the residual native oxide was confirmed, resulting in the formation of stoichiometric Al₂O₃ by annealing, which can be effective for obtaining a well behaved interface.

FIG. 5. (a) XPS As 3d and (b) Al 2p spectra for surfaces after 4-cycle ALD-Al₂O₃ process with and without annealing.

References: