Equivalent-Circuit Generation from Finite Element Solution Using Proper Orthogonal Decomposition

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This paper presents generation of equivalent circuits from finite element (FE) model of electromagnetic devices using proper orthogonal decomposition (POD). This method effectively computes the frequency response of the reduced FE model constructed by POD-based model order reduction. Then the circuit parameters are determined so as to minimize the error between the frequency responses of the reduced FE model and equivalent circuit. The frequency characteristics of an inductor and induction heating machine evaluated by the equivalent circuit are shown to be in good agreement with those computed from the original FE model.

Index Terms— Equivalent circuit, finite element method, induction heating, model order reduction.

I. INTRODUCTION

FINITE element method (FEM) has been widely performed for design of electric machines and devices. However, due to its long computational time, equivalent circuits are frequently used rather than FEM for design of driving and control circuits. When deriving equivalent circuits of electric devices such as inductors and reactors, for example, the circuit parameters are determined from resistance and reactance computed by FEM [1,2]. It would be, however, difficult to accurately express its frequency characteristics over wide range using this conventional method.

Recently the equivalent circuit of ladder configuration has been directly derived from the analytical expression of eddy currents in electrical steel sheets [3]. This method would be valid for fields in simple geometry which can be expressed in a closed form. Now, a question arises: is it possible to generate equivalent circuits not only from closed-form solutions but also from FE solutions? Such equivalent circuits could be generated if the frequency characteristics are available. However, for large FE models, we need heavy computations to obtain the frequency characteristics over sufficiently wide range.

In this paper, we propose equivalent-circuit generation from FE solutions using model order reduction (MOR) [4,5] based on proper orthogonal decomposition (POD). In this method, fields are expressed as a linear combination of small number of basis vectors obtained from the field snapshots [6-9]. In the proposed method, the computational time necessary for FE analysis is reduced by POD-based MOR. This method allows us to effectively compute the frequency responses for circuit generation. The circuit parameters are then determined so as to minimize the error between the frequency responses of the reduced FE model and equivalent circuit.

In this study, we apply the proposed method to a simplified three dimensional inductor model and compare the accuracy and computational time of the resultant equivalent circuit with those of the original FE model. Moreover, we apply it to the three-dimensional model of an induction heating (IH) machine to obtain the frequency-dependent power factor and joule loss.

II. NUMERICAL METHOD

A. Proper orthogonal decomposition

Let us consider quasi-static electromagnetic fields described by FE equations in frequency domain given by

\[ V = \int_\Omega \sum_i a_i \left( \frac{\partial N_j}{\partial x} \cdot \frac{\partial N_i}{\partial x} + j \omega \kappa \mu N_i \cdot N_j \right) \, dV + \int_\Omega \sum_k \varphi_k \left( \frac{\partial N_j}{\partial x} \cdot \nabla N_k \right) \, dV = \int_\Omega N_j \cdot J \, dV \]

\[ \sum_i a_i \left( \int_\Omega \frac{\partial N_j}{\partial x} \cdot \nabla N_i \, dV + \int_\Omega \frac{\partial N_i}{\partial x} \cdot \nabla N_j \, dV \right) + \int_\Omega \sum_k \varphi_k \left( \frac{\partial N_k}{\partial x} \cdot \nabla N_j \right) - \int_\Omega \sum_k \varphi_k \left( \frac{\partial N_j}{\partial x} \cdot \nabla N_k \right) \, dV = 0 \]

where \( a_i, N_i, \kappa, \mu, \varphi_k \) and \( J \) denote magnetomotive force along an edge \( i \), edge basis function, magnetic reluctivity, electric conductivity, scalar potential and current density. The electromagnetic field is assumed to be coupled with a circuit governed by

\[ V = Ri + L \frac{di}{dt} + \frac{d\Phi}{dt} \]

where \( V, R, L, i \) and \( \Phi \) denote input voltage, external resistance and inductance, current and the magnetic flux computed from \( a_i \). Eqs. (1), (2) and (3) are expressed in a matrix form as

\[ K(j\omega)x = b(j\omega) \]

where \( K \in \mathbb{R}^{m \times n}, x \in \mathbb{R}^n, b \in \mathbb{R}^m \) are FE matrix, unknown and source vectors, respectively. We solve (4) at \( s \) different
frequencies to construct the data matrix $X$ which is composed of the snapshotted fields as follows:

$$X = [x_1(\omega_1) \ x_2(\omega_2) \ \cdots \ x_r(\omega_r)]$$

(5)

The singular value decomposition applied to $X$ results in

$$X = W\Sigma V^T = \sigma_i w_i v_i^T + \sigma_2 w_2 v_2^T + \cdots + \sigma_s w_s v_s^T$$

(6)

where $\sigma_i$ is $i$-th singular value of $X$ and $w_i, v_i$ are the eigenvectors of $XX^T$ and $X^TX$, respectively. The unknown $x$ is now expressed as a linear combination of $w_i$; that is, $x = W^T y$ where $y \in \mathbb{R}^s$. Thus (4) becomes

$$W^T K(j\omega) W y = W^T b(j\omega)$$

(7)

The snapshot number $s$ is set much smaller than $n$ so that one solves (7) much faster than (4) to obtain the frequency characteristics.

B. Equivalent-circuit generation

We derive the equivalent circuit from the frequency response obtained by solving (7). We employ here Foster- and Cauer-form networks shown in Fig. 1 [10]. In Foster realization, admittance $Y(j\omega)$ is expressed by

$$Y(j\omega) \approx \sum_{k=1}^{q} \frac{1}{R_k + j\omega L_k}$$

(8)

where $R_k$, $L_k$ and $q$ denote resistance, inductance and number of the stage of the ladder circuit, respectively. On the other hand, in Cauer realization, impedance $Z(j\omega)$ is expressed in a form of continued fraction as

$$Z(j\omega) \approx j\omega L_1 + \frac{1}{R_1 + \frac{1}{j\omega L_2 + 1/(R_2 + \cdots)}}$$

(9)

To determine the circuit parameters $R_1, \cdots, R_q$ and $L_1, \cdots, L_q$, in (8) and (9), we solve the optimization problem defined by

$$f(R, L) = \sqrt{\sum_{i=1}^{M} |G_{FEM}(j\omega_i) - G(j\omega_i, R, L)|^2} \rightarrow \text{min.}$$

(10)

sub. to $R_i, L_k \geq 0$

where $R = [R_1, R_2, \cdots, R_q]$, $L = [L_1, L_2, \cdots, L_q]$, $G_{FEM}(j\omega_i)$, $G(j\omega_i, R, L)$ represent either $Y$ or $Z$ obtained by solving (7) and from the derived equivalent circuit and $M$ is the number of sampling points for circuit identification, where $M > s$. The optimization problem (10) is solved here using the real coded genetic algorithm (RGA). The process of the proposed method is illustrated in Fig. 2.

III. NUMERICAL RESULTS

To test performance, the proposed method is applied to models of an inductor and IH machine.

A. Simplified inductor model

Let us consider an inductor connected to the simple circuit shown in Fig. 3. The model parameters are summarized in Table I. The magnetic core is assumed to be conductive although eddy currents in the coil windings are neglected.

Dependence of the relative error, $e = \|x_{FEM} - x_{\text{reduced}}\| / \|x_{FEM}\|$, of the reduced model on $s$ is shown in Fig. 4. It can be seen in Fig.4 that the error is improved little even if $s$ is increased more than 6. For this reason, we take 6 snapshots with equal frequency intervals for $1 \leq f \leq 10^3$ Hz. Then, we solve (7) at 11 frequency points, $f = 1, 100, 200, \ldots, 10^3$ Hz, to obtain the frequency characteristic of the inductor. That is, $s=6, M=11$.

The frequency characteristics of the current obtained from the conventional FEM and the proposed method for different number of ladder stages $q$ are shown in Fig.5. The proposed...
Computational accuracy of the proposed method improves as the number of points for frequency sweep increases. Computational times of the conventional FEM and the proposed method are summarized in Table III. It is also possible to perform transient analysis using the resultant equivalent circuit. The results are shown in Fig. 6, where we again find good correspondence among the results. Computational accuracy of the proposed method improves with $s$ and $M$. The lower error bound estimated \textit{a posteriori} is about $10^{-2}$.

\begin{table}[h]
\centering
\small
\begin{tabular}{cccccc}
\hline
\textbf{freq} & $R_1$ & $R_2$ & $R_3$ & $R_4$ & $R_5$ \\
\hline
5 & $1.18\times10^3$ & $4.91\times10^4$ & $1.73\times10^4$ & $5.59\times10^4$ & $1.81\times10^4$ \\
3 & $3.35\times10^4$ & $1.10\times10^5$ & $1.55\times10^4$ & ---- & ---- \\
2 & $1.48\times10^4$ & $1.08\times10^5$ & ---- & ---- & ---- \\
\hline
\end{tabular}
\caption{Foster circuit parameters}
\end{table}

\begin{table}[h]
\centering
\small
\begin{tabular}{cccccc}
\hline
\textbf{freq} & $R_1$ & $R_2$ & $R_3$ & $R_4$ & $R_5$ \\
\hline
5 & $3.68\times10^8$ & $1.44\times10^8$ & $8.74\times10^8$ & $9.10\times10^8$ & $6.73\times10^8$ \\
3 & $1.74\times10^8$ & $3.48\times10^8$ & $6.98\times10^8$ & ---- & ---- \\
2 & $2.56\times10^8$ & $3.25\times10^8$ & ---- & ---- & ---- \\
\hline
\end{tabular}
\caption{Cauer circuit parameters}
\end{table}

\begin{table}[h]
\centering
\small
\begin{tabular}{cccc}
\hline
\textbf{method} & $M$ & Computational time [min]* \\
\hline
Conventional FEM & ---- & 120 \\
Proposed method & 21 & 21.0 \\
& 16 & 16.4 \\
& 11 & 11.8 \\
\hline
\end{tabular}
\caption{Elapsed times for computations at 100 different frequencies}
\end{table}

*Xeon X5660(2.8GHz) is used.

\textbf{B. Induction heating machine}

We next consider an IH machine model shown in Fig. 7, where a conducting sheet is heated up by Joule losses caused by eddy currents. The number of the unknowns in the original FE model are 741264. The AC currents flowing through the coil windings generate time variations in the magnetic flux, and eddy currents in the sheet. For the effective control of the IH machine, power factor which is the ratio of active power to reactive one is required to be as high as possible. Moreover, evaluation of the Joule loss is of importance for on-line control of the sheet temperature.

The power factor $\eta$ and Joule loss $P$ are computed by the conventional FEM as well as the equivalent circuit generated by the proposed method. They are computed from

$$p^{FEM} = \sum_{n} \left| E_n \right|^2 dv$$  \hspace{1cm} (12)
where $p_{\text{FEM}}$, $p_{\text{Circuit}}$, $n_e$, $E$, $V$ and $i$ denote Joule losses computed from FE analysis and equivalent circuits, number of unknowns, the electric field, voltage and complex conjugate of current. The conditions of the snapshots and sampling points to generate the equivalent circuits are the same as those mentioned in III-A.

The frequency characteristics of $\eta$ and $P$ computed from the FEM and equivalent circuits are shown in Figs. 8 and 9 where $V=1kV$ and resultant circuit parameters are summarized in Table IV. Figs. 8 and 9 indicate that the results obtained from the equivalent circuits are in good agreement with those obtained by conventional FEM. In particular, the equivalent circuit gives the peaks of $\eta$ accurately although it is generated based on just three snapshots. Once the equivalent circuit is established, $\eta$ and $P$ can be evaluated much faster than FEM.

\[ P_{\text{Circuit}} = \frac{VI^*}{2} \]  

(13)

IV. CONCLUSION

In this paper, we have proposed equivalent-circuit generation from reduced FE models obtained by POD-based MOR. The frequency characteristic of these devices are effectively obtained by solving reduce FE equations whose unknowns are the weighting coefficients to the basis vectors. The circuit parameters of Foster and Cauer circuits are determined from the resultant frequency characteristics using GA. We have applied the proposed method to analysis of the inductor and IH machine under quasi-static approximation. The equivalent circuits derived from the proposed method is shown to give accurate results when sufficient number of ladder stage is used. Once the equivalent circuit is established, we can evaluate various properties such as power factor and loss much faster than the conventional FEM. The extension of the proposed method to nonlinear problems will be future task.

<table>
<thead>
<tr>
<th>TABLE IV: Circuit parameters</th>
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<tbody>
<tr>
<td>Circuit</td>
</tr>
<tr>
<td>Foster</td>
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<tr>
<td>2</td>
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<tr>
<td>Cauer</td>
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REFERENCES


