Characterization of capture cross sections of interface states in dielectric/III-nitride heterojunction structures

M. Matys, R. Stoklas, J. Kuzmik, B. Adamowicz, Z. Yatabe, and T. Hashizume

Citation: Journal of Applied Physics 119, 205304 (2016); doi: 10.1063/1.4952708

View online: http://dx.doi.org/10.1063/1.4952708

Articles you may be interested in

Correlation of interface states/border traps and threshold voltage shift on AlGaN/GaN metal-insulator-semiconductor high-electron-mobility transistors

Defect states characterization of non-annealed and annealed ZrO2/InAlN/GaN structures by capacitance measurements
Appl. Phys. Lett. 102, 063502 (2013); 10.1063/1.4792060

Effects of interface states and temperature on the C - V behavior of metal/insulator/AlGaN/GaN heterostructure capacitors
J. Appl. Phys. 103, 104510 (2008); 10.1063/1.2924334

Real-space electron transfer in III-nitride metal-oxide-semiconductor-heterojunction structures
Appl. Phys. Lett. 87, 043505 (2005); 10.1063/1.2001745

Surface passivation of GaN and GaN/AlGaN heterostructures by dielectric films and its application to insulated-gate heterostructure transistors
Characterization of capture cross sections of interface states in dielectric/III-nitride heterojunction structures

M. Matys,1,2 R. Stoklas,1,3 J. Kuzmik,3 B. Adamowicz,2 Z. Yatabe,1 and T. Hashizume1

1Research Center for Integrated Quantum Electronics, Hokkaido University, Kita-13 Nishi-8, Kita-ku, 060-8628 Sapporo, Japan
2Surface Physics and Nanostructures Department, Institute of Physics-CND, Silesian University of Technology, Konarskiego 22B, 44-100 Gliwice, Poland
3Institute of Electrical Engineering, Slovak Academy of Sciences, Dubravská cesta 9, 841 04 Bratislava, Slovak Republic

(Received 26 February 2016; accepted 14 May 2016; published online 31 May 2016)

We performed, for the first time, quantitative characterization of electron capture cross sections \( \sigma \) of the interface states at dielectric/III-N heterojunction interfaces. We developed a new method, which is based on the photo-assisted capacitance-voltage measurements using photon energies below the semiconductor band gap. The analysis was carried out for AlGaN/GaN metal-insulator-semiconductor heterojunction (MISH) structures with \( \text{Al}_2\text{O}_3 \), SiO\(_2\), or SiN films as insulator deposited on the AlGaN layers with Al content (\( x \)) varying over a wide range of values. Additionally, we also investigated an \( \text{Al}_2\text{O}_3/\text{InAlN/GaN} \) MISH structure. Prior to insulator deposition, the AlGaN and InAlN surfaces were subjected to different treatments. We found that \( \sigma \) for all these structures lies in the range between \( 5 \times 10^{-15} \) and \( 10^{-16} \) cm\(^2\). Furthermore, we revealed that \( \sigma \) for dielectric/Al\(_{1-x}\)Ga\(_x\)-N interfaces increases with increasing \( x \). We showed that both the multiphonon-emission and cascade processes can explain the obtained results. Published by AIP Publishing.

[http://dx.doi.org/10.1063/1.4952708]

I. INTRODUCTION

AlGaN and InAlN/GaN high electron mobility transistors (HEMTs) surpass Si devices for high voltage and high speed switching applications due to the exceptional physical properties of nitrides, which include their wide band gaps and stability at high temperatures.1–4 It was demonstrated by many groups that GaN-HEMTs with an insulated gate (IG) structure, apart from being necessary for achieving normally off operation, exhibit important advantages over Schottky-gate-based GaN-HEMTs such as lower gate leakage current, higher breakdown voltage, better thermal stability of the gate and less current collapse.6–10 In order to obtain a good IG structure, suitable insulating materials such as \( \text{Al}_2\text{O}_3 \), SiO\(_2\), and SiN ensuring a large band offset and high permittivity are widely applied.11–13 Furthermore, the chosen insulator material should provide a high quality, stable insulator/semiconductor interface with a low density of interface electronic states. Because of the strong impact of these states on the performance and reliability of HEMTs, monitoring the dynamical properties of insulator/semiconductor interfaces is highly desirable.

However, the characterization of interface states is extremely difficult by standard electrical methods for passivated III-N heterojunction structures due to the presence of two interfaces and very long emission time constants from the deep states at room temperature (RT).10–16 As a consequence, there is still a lack of understanding of interface state fundamental properties at dielectric/III-N heterojunction interfaces, especially their dynamic trapping/emitting behavior expressed in terms of capture cross sections. Furthermore, it should be noted that, as a matter of fact, there is just only one report17 about the values of the capture cross section for dielectric/III-N heterojunction interfaces. Consequently, in the studies and modeling of the operation of IG-type GaN-based HEMTs, the capture cross section values typical for the SiO\(_2\)/Si interface or bulk defect levels are often inadequately assumed.18–23

The aim of the present work is the determination and analysis of the electron capture cross sections (\( \sigma \)) of interface states at dielectric/III-N heterojunction interfaces using a newly developed photo-electric method. This method is based on the measurement of the threshold voltage shift \( \Delta V_{th} \) in a capacitance-voltage (C-V) characteristics before and after illumination using photon energies \( E_{ph} \) less than the band gap \( E_g \) at high temperature (T). In our studies, we used two types of metal-insulator-semiconductor heterojunction (MISH) structures: (i) AlGaN/GaN based structures with \( \text{Al}_2\text{O}_3 \), SiO\(_2\), and SiN films as insulator deposited by various techniques on the Al\(_{1-x}\)Ga\(_x\)-N layers with an Al content varying over a wide range (\( x \)) from 0.15 to 0.4 and (ii) an \( \text{Al}_2\text{O}_3/\text{InAlN/GaN} \) MISH structure. We found that \( \sigma \) for the examined structures varied from \( 5 \times 10^{-19} \) to \( 10^{-16} \) cm\(^2\). Furthermore, we revealed that \( \sigma \) increased with the increasing lattice mismatch in heterostructures. Finally, we demonstrated that both the multiphonon-emission and cascade processes can explain the obtained results.

II. SAMPLE STRUCTURE AND FABRICATION PROCESS

Figure 1 shows a schematic illustration of the MISH structures used in this study, i.e., (i) \( \text{Al}_2\text{O}_3/\text{Al}_{0.2}\text{Ga}_{0.8}\text{N}/\text{GaN} \) with a metal-organic chemical vapor deposition (MOCVD) prepared AlGaN layer (thickness of 34 nm) with and without
inductively coupled plasma (ICP) dry etching (control sample) at RT;24 (ii) Al₂O₃/In₀.19Al₀.81N/GaN with an MOCVD-prepared InAlN layer (12 nm);25 (iii) SiO₂/AlₓGa₁₋ₓN/N/GaN with 25 nm thick modulation doped AlGaN layers (x = 0.15, 0.26, 0.40); and (iv) SiN/AlₓGa₁₋ₓN/N/GaN with 25 nm thick modulation doped AlGaN layers (x = 0.15, 0.26, 0.40). The details of the examined structures are summarized in Table I.

We used atomic layer deposition (ALD), plasma-enhanced chemical vapor deposition (PECVD), and electron cyclotron resonance chemical vapor deposition (ECR CVD) for deposition of Al₂O₃, SiO₂, and SiN layers, respectively.

All heterojunction samples were cleaned with an HF solution before the insulator deposition in order to mitigate native oxides at the air-exposed AlGaN and InAlN surfaces. The HF treatment was chosen as more efficient in removal of native oxides compared to other treatments using hydrochloric acid or ammonium hydroxide in the case of InAlN and GaN.26,27 The applied chemical approach was realized by dipping the sample in the HF solution for 1 min at RT, then rinsing it in deionized water, followed by a blowing process with pure nitrogen. The X-ray photoelectron spectroscopy study showed that the HF treatment is effective in reducing native oxides at the GaN-based alloys.26,27

Ohmic contacts were ring-shaped Ti/Al/Ti/Au multilayers and gate contacts were either Al/Au or Ni/Au circles with diameters from 200 to 500 μm. An ohmic annealing was carried out at 830°C for 2 min in N₂. For all types of (i) and (ii) heterostructures, we prepared a 10 nm thick SiN protection film deposited by ECR CVD to avoid damages of AlGaN and InAlN surfaces during ohmic contact annealing.16,28 After the annealing, the SiN film was removed using buffered HF solution.

### III. EXPERIMENT

The C-V characteristics were measured at 1 MHz by an Agilent 4294A Precision Impedance Analyzer at different temperatures. Figure 2 shows typical C-V characteristics of the Al₂O₃/AlGaN/GaN structures with and without ICP etching of AlGaN at RT. For practical processing of fabricating normally off AlGaN/GaN HEMTs, a recessed gate structure is often used. The critical processes for fabricating such devices are etching to form the recessed gate region and the subsequent formation of an insulator-semiconductor interface. Since the interface quality markedly affects the transistor performance, it is important to understand the relationship between etching technology and resulting interface electronic properties, including interface cross sections.

We observed the C-V curves with two steps which are typical for MISH structures. The nearly flat capacitance at the reverse bias corresponds to the total capacitance of Al₂O₃ and AlGaN layers (C_total) in series, while the constant capacitance at the forward bias is determined by the Al₂O₃ oxide capacitance (C_{Al₂O₃}). When the gate voltage (V_G) approached the threshold voltage (V_{th}) in the negative bias range, the capacitance rapidly decreases to nearly zero, signaling the depletion of the two-dimensional electron gas (2DEG) at the AlGaN/GaN interface. In addition, from Fig. 2, it follows that the sample with ICP etching exhibited increased C_total and significant V_{th} shift in the C-V curve compared with the sample without ICP etching due to the reduced AlGaN layer thickness. It was shown from transmission electron microscopy observation29 that the sample without etching exhibited a flat Al₂O₃/AlGaN interface, indicating the low-energy and layer-by-layer characteristics of the ALD process. On the contrary, the ICP etching resulted in the slight roughening of the interface, creating monolayer-level steps at the AlGaN surface. As indicated by the less C-V slope in Fig. 2, the ICP-etched

![FIG. 1. Scheme of the cross-sectional view of MISH structures used in this study.](image)

![FIG. 2. C-V characteristics of the sample 1 and 2.](image)

**TABLE I. The details of examined structures.**

<table>
<thead>
<tr>
<th>Sample</th>
<th>Barrier</th>
<th>Barrier thickness (nm)</th>
<th>Insulator (thickness, nm)</th>
<th>Remarks</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Al₀.₂Ga₀.₈N</td>
<td>34</td>
<td>Al₂O₃ (20)</td>
<td>w/o ICP</td>
</tr>
<tr>
<td>2</td>
<td>Al₀.₂Ga₀.₈N</td>
<td>27</td>
<td>Al₂O₃ (20)</td>
<td>w/ICP</td>
</tr>
<tr>
<td>3</td>
<td>In₀.₁₉Al₀.₈₁N</td>
<td>12</td>
<td>Al₂O₃ (20)</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>Al₀.₁₅Ga₀.₈₅N</td>
<td>25</td>
<td>SiO₂ (22)</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>Al₀.₂₆Ga₀.₇₄N</td>
<td>25</td>
<td>SiO₂ (22)</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>Al₀.₄₀Ga₀.₆₀N</td>
<td>25</td>
<td>SiO₂ (22)</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>Al₀.₁₅Ga₀.₈₅N</td>
<td>25</td>
<td>SiN (22)</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>Al₀.₂₆Ga₀.₇₄N</td>
<td>25</td>
<td>SiN (22)</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>Al₀.₄₀Ga₀.₆₀N</td>
<td>25</td>
<td>SiN (22)</td>
<td></td>
</tr>
</tbody>
</table>
sample exhibited the higher interface state density probably due to interface disorder.\textsuperscript{16,24,29}

The $\Delta V_{th}$ in C-V characteristics was measured in the following manner. The measurement steps and the corresponding band diagrams are schematically shown in Figs. 3 and 4, respectively. At first, before illumination, we swept the gate voltage from $V_G = 0$ V to accumulation and then back to depletion to obtain the reference $V_{th}$ under dark condition. Subsequently, we swept the gate voltage from depletion to accumulation in order to fill the interface states with electrons, as indicated by the broken curve (step 1) in Fig. 3. Then, we swept the gate voltage from accumulation to $V_G = 0$ V and kept this bias during the time ($t_{emit}$) of about 1 h (step 2 in Fig. 3). Due to thermal emission for the time of $t_{emit}$, as shown in Fig. 4(b), the interface states between the conduction band (CB) and energy $E_{im}$ described by the Shockley-Read-Hall (SRH) statistics are empty of electrons. The $E_{im}$ value is given as follows:

$$E_{im} = kT \ln(v_{th} \sigma NC_{t_{emit}}),$$

where $v_{th}$ is the electron thermal velocity and $NC$ is the effective density of states in CB.

Finally, we illuminated the structure by monochromatic light with $E_{ph} < E_g$, for 10 min while keeping $V_G = 0$ V in order to empty the interface states distributed below $E_{im}$ of electrons, as shown in Fig. 4(c). Immediately after switching off the light, we restarted the C-V sweeping towards depletion under dark condition (step 3 in Fig. 3). As a light source, MAX-302 (ASAHI Spectra) equipped with a xenon lamp and a set of band-pass filters ($1.26$ eV $< E_{ph} < 2.2$ eV) was used. Under illumination, we clearly observed the parallel C-V curve shift towards the negative bias direction. The $\Delta V_{th}$ value was determined as the difference between the $V_{th}$ after and before illumination. This process was repeated at different $T$ as well as for different $E_{ph}$, and on this basis, we obtained the $E_{ph}$ and $T$-dependence of $\Delta V_{th}$.

IV. METHOD FOR DETERMINATION OF CAPTURE CROSS SECTIONS AND RESULTS

The main idea of the method developed for the determination of $\sigma$ is based on the estimation of the $E_{im}$ position from the measurement of light-induced $\Delta V_{th}$ in the C-V curves at high $T$. In Fig. 5, we showed the C-V curves before and after illumination with $E_{ph}$ of 1.4 eV and 2.2 eV at RT and $T = 573$ K. In Fig. 6, we then summarized the results of light-induced $\Delta V_{th}$ vs. $T$. For all the structures, we observed that light-induced $\Delta V_{th}$ strongly decreased with increasing $T$. At $T = 573$ K, for $E_{ph} = 1.4$ eV, $\Delta V_{th}$ is approximately equal to zero, indicating that light has almost no influence on $V_{th}$ in this particular case.

The fundamental reason of the reduction of $\Delta V_{th}$ with increasing $T$ is the changing occupation of the interface states vs. $T$. For example, during illumination by the photons with energy $E_{ph}$, at given $T$, the interface states distributed between $E_{ph}$ and $E_{im}$ become depopulated of electrons, as shown in Fig. 7. This results in the change of the interface charge $\Delta Q_{it} = q \int_{E_{ph}}^{E_{im}} D_{it}(E) dE$ (q is the elementary charge) and consequently in the inducing of $\Delta V_{th}$ equal to

$$\Delta V_{th} = \Delta Q_{it}/C_{total}.$$  \text{(2)}

It is obvious that the increasing $T$ causes decreasing $\Delta Q_{it}$ due to lowering of $E_{im}$ and thus reducing $\Delta V_{th}$. From the fact that for $T = 573$ K and $E_{ph} = 1.4$ eV, $\Delta V_{th}$ is approximately equal to zero, we can conclude that $E_{im} \approx 1.4$ eV at this $T$. On the other hand, based on the approach proposed by Mizue et al.,\textsuperscript{13} we determined $D_{it}(E)$ between 1.2 and 2.2 eV from $E_{ph}$-dependence of $\Delta V_{th}$ and carried out linear approximation of $D_{it}(E)$, i.e., $D_{it}(E) = A + BE$, where A and B are the fitting

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{image}
\caption{Schematic illustration of $\Delta V_{th}$ measurement.}
\end{figure}
coefficients, as shown in Fig. 8. Therefore, using Eq. (2) we can write that
\[ \Delta V_{th} = \frac{q(\Delta E + B (\Delta E)^2)}{C_{total}}, \]
where \( \Delta E = 2.2 \text{ eV} - E_{im} \). From Eq. (3), we obtain the following relationship for \( E_{im} \):
\[ E_{im} = 2.2 \text{ eV} + \frac{A}{B} - \sqrt{\left(\frac{A}{B}\right)^2 + 2 \frac{C_{total}}{qB} \Delta V_{th}}. \]

Using Eqs. (1) and (4), we determined \( \sigma \), which can be interpreted as the average value for the interface states between CB and near the midgap.

The obtained results of \( \sigma \) as a function of the lattice mismatch in the examined AlGaN/GaN and InAlN/GaN...
heterostructures are summarized in Fig. 9. The value of $r$ was calculated using the values of $A$ from $1 \times 10^{11}$ to $5 \times 10^{11}$ cm$^{-2}$ eV$^{-1}$ and $B$ from $1 \times 10^{11}$ to $5 \times 10^{12}$ cm$^{-2}$ eV$^{-2}$. In addition, in order to verify the validity of the proposed method, we performed the calculations of the C-V characteristics at RT and $T = 573$ K using the determined $r$ and $D_{it}(E)$. The calculated C-V curves were obtained from a numerical solution of the Poisson’s equation by finite element method with Neumann boundary conditions at the interfaces, i.e., insulator/AlGaN and insulator/InAlGaN as well as AlGaN/GaN and InAlGaN/GaN, which were determined by $D_{it}(E)$ and fixed charge density originating from spontaneous and piezoelectric polarization. The calculation reproduced well the measured C-V characteristics, which is an independent confirmation of the reliability of the developed method. It should be emphasized that such a sensitivity of $r$ on the mismatch variations should be taken into account in the design of GaN-based HEMTs because of the strong impact of $r$ on the device performance.

V. DISCUSSION

In general, the non-radiative electron capture and emission from interface states at SiO$_2$/Si interface is explained in terms of the multi-phonon emission model introduced by Henry and Lang as well as the cascade mechanism proposed by Lax, which was later developed by Leningrad (currently Sankt Petersburg) group. According to the multi-phonon emission model, $r$ is thermally activated and given by

$$r = r_T = 1 \exp \left( -\frac{E_b}{kT} \right),$$

where $E_b$ is the thermal barrier for capture and can be approximately expressed as the following equation:

$$E_b = \frac{(E_i - Sh\nu)^2}{4Sh\nu},$$

where $E_i$ is the ionization energy of the state, $h\nu$ is the average phonon energy, and $S$ is the so called Huang-Rhys factor which is a measure of the strength of electron-phonon coupling. The $E_b$ is defined on the configuration diagram (Fig. 10) as being the energy at the crossing of two curves representing the total electronic energy for the initial state before capture and final state after capture.

In the framework of Lax’s model, the capture process can be divided into two steps (Fig. 11). At first, electron from CB is attracted by the long-range Coulomb potential of which cannot be approximated by the straight line, one can use a polynomial fitting.

From the above results (Fig. 9), we can conclude that: (1) $\sigma$ for the interface states at dielectric/III-N heterojunction interfaces is in the range of $5 \times 10^{-13}$ to $10^{-16}$ cm$^2$, (2) $\sigma$ increases with increasing lattice mismatch in the heterostructures for both ALD- and CVD-prepared insulator layers, and (3) for oxide/III-N heterojunction interfaces takes similar values and almost remains unchanged after the ICP surface treatment. It should be emphasized that such a sensitivity of $\sigma$ on the mismatch variations should be taken into account in the design of GaN-based HEMTs because of the strong impact of $\sigma$ on the device performance.
the trap. Next, under the assumption that the trap possesses a series of closely separated hydrogen-like excited states, electron jumps to lower states losing its energy by emission of phonons. In both first and second steps, the electron can be regarded as a classical particle. The $\sigma$ value of this process can be approximately expressed as

$$\sigma = 4\pi \left( \frac{Zq^2}{mc^2} \right)^3 \left( \frac{mc^2}{kT} \right)^5 \frac{1}{L},$$

(7)

where $Z$ is the atomic number, $qZ$ is the charge of trap, $c$ is the velocity of light, $m$ is the effective mass of electron, $L$ is the mean free path of the electron in the case of collisions with acoustic phonon.

It is evident that both above models can explain the dependencies of $\sigma$ versus mismatch. For example, from Eqs. (5) and (6) it follows that due to the increase of $S$, $E_b$ is reduced and, as a result, $\sigma$ increases. In other words, $\sigma$ is larger for the interface at which the multiphonon process occurs with the stronger lattice distortion (higher $Shv$, Fig. 10). In addition, the multiphonon-emission capture process, related to a lower $E_{b,\nu}$ can be enhanced at interfaces with the higher degree of mismatch due to inducing strains resulting in larger $\sigma$. This explanation can be supported by the fact that the examined structures contained thin barriers layers ($Al_Ga_{1-x}N$ and $In_{x}Al_{1-x}N$) of about 25 nm in thickness, and thus fully strained for $x$ up to about 0.4, according to the literatures. On the contrary, from the theoretical calculation and capacitance/conductivity measurements it is known that the bulk mobility ($\mu$) in AlGaN decreases with $x$ due to alloy-scattering limited. Therefore, because $L \sim \mu$ we expect that L also decreases with $x$ and thus $\sigma$ increases with $x$, according to Eq. (7).

As for the $Al_2O_3/AlGaN$ interfaces with and without ICP etching of AlGaN, our previous work showed that the ICP-etched sample exhibited the higher interface state density probably due to the interface disorder. However as shown in Fig. 9, the values of capture cross section remained similar among these samples. This indicates that a lattice distortion due to the heterojunction mismatch dominantly contributes to a mechanism determining the capture cross section. Furthermore, one of the possible reasons for difference observed in the $\sigma$ values between oxides and SiN insulators is the compressive stress, which occurs in the SiN layer more markedly. This effect can lead to the reduction of the total strain in AlGaN barrier. Consequently, the multiphonon process can occur with the weaker lattice distortion (lower $Shv$, Fig. 10) at the SiN/AlGaN interface, resulting in the lower $\sigma$ than those of oxides/AlGaN interfaces.

VI. CONCLUSIONS

In conclusion, we performed, for the first time, quantitative studies of the carrier capture cross sections of the interface states at dielectric/III-N heterojunction interfaces using a developed photo-electric method. This method is based on the photo-assisted capacitance-voltage measurements using photon energies below the semiconductor band gap. The analysis was carried out for AlGaN/GaN MISH structures with $Al_2O_3$, $SiO_2$, or $SiN$ films as insulator deposited on the AlGaN layers with Al content ($x$) varying from 0.15 to 0.40. Additionally, we also investigated an $Al_2O_3/InAlN/GaN$ MISH structure. Prior to insulator deposition, the AlGaN and InAlN surfaces were subjected to different treatments. We found that $\sigma$ for all these structures lies in the range of $5 \times 10^{-19}$ to $10^{-16}$ $cm^2$. We also obtained that $\sigma$ for oxide/III-N interfaces takes similar values, which remain almost unchanged even after surface treatment. Furthermore, we revealed that $\sigma$ for dielectric/III-N heterojunction interfaces exhibits an increase with the increasing lattice mismatch in heterostructures. We showed that both the multiphonon-emission and cascade processes can explain the obtained results. These results are crucial to understand the dynamical properties of interface states and should be taken into account in the design of IG-type GaN-based HEMTs.

ACKNOWLEDGMENTS

This work was partially supported by the National Science Center in Poland within Project No. NCN 515606339 and the Visegrad Group (V4)-Japan Joint Research Program on Highly Safe GaN Metal-Oxide-Semiconductor Transistor Switch. The authors thank Professor M. Akazawa for the $Al_2O_3/InAlN/GaN$ structure.