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Precise thickness control in recess etching of AlGaN/GaN hetero-structure using photocarrier-regulated electrochemical process

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The photocarrier-regulated electrochemical (PREC) process was developed for fabricating recessed-gate AlGaN/GaN high-electron-mobility transistors (HEMTs) for normally off operation. The PREC process is based on photo-assisted electrochemical etching using low-energy chemical reactions. The fundamental photo-electrochemical measurements on AlGaN/GaN heterostructures revealed that the photo-carriers generated in the top AlGaN layer caused homogeneous etching of AlGaN with a smooth surface, but those generated in the GaN layer underneath caused inhomogeneous etching that roughens the surface. The concept of the PREC process is to supply the photo-carriers generated only in the AlGaN layer by selecting proper conditions on light wavelength and voltage. The phenomenon of self-termination etching has been observed during the PREC process, where the etching depth was controlled by light intensity. The recessed-gate AlGaN/GaN HEMT fabricated with the PREC process showed positive threshold voltage and improvement in transconductance compared to planar-gate AlGaN/GaN HEMTs. Published by AIP Publishing. [http://dx.doi.org/10.1063/1.4983013]

I. INTRODUCTION

AlGaN/GaN high-electron-mobility transistors (HEMTs) are promising candidates for high-power electronics applications because high blocking voltage yet low on-resistance \(R_{ON}\) can be achieved.\(^1\)\(^-\)\(^4\) From the fail-safe viewpoint of power-switching devices, it is necessary to attain normally off operation. One of the promising approaches to attain such operation is adopting a recessed-gate structure,\(^5\)\(^-\)\(^7\) which can be fabricated by thinning the AlGaN layer beneath the gate electrode. The dry etching process is commonly used for thinning the AlGaN layer because the wet etching process is not applicable due to the chemical stability of group-III nitrides.\(^8\)\(^-\)\(^10\) However, dry-etched surfaces are generally negatively affected by various types of damages, which may lead to degradation of the device’s performance.\(^11\),\(^12\) Moreover, unintentional variations in the recess depth make it difficult to precisely control the threshold voltage \(V_{th}\).

One alternative approach is photo-assisted electrochemical (PEC) etching, which is a cyclic process consisting of anodic oxidation and subsequent dissolution of the resulting oxide in an electrolyte. Compared to dry etching, PEC etching is highly desirable in its simplicity and the absence of plasma damage.\(^13\),\(^14\) Also, this etching is applicable to various semiconductors, even chemically stable materials such as group-III nitrides.\(^15\)

Recessed-gate AlGaN/GaN HEMTs using PEC etching have recently been reported. Chiou et al.\(^16\) improved transconductance \(g_m\) by PEC etching and subsequent PEC oxidation. Zhang et al.\(^17\) showed the suitability of PEC etching in ionic liquid etchant by developing high-performance normally off HEMTs. We previously attained normally off operation in recessed-oxide gate structures formed by PEC oxidation in parallel with PEC etching conducted in the same electrolyte.\(^18\)

Despite the fact that device performances of recessed-gate AlGaN/GaN HEMTs fabricated by PEC etching have been demonstrated, important aspects for recess etching, such as depth-controllability and surface roughness, have not been sufficiently investigated. Although PEC etching is caused by photo-carriers, carrier transfer in an AlGaN/GaN heterostructure is not fully understood, which makes it difficult to optimize electrochemical conditions to obtain desirable etching features.

In this study, we investigated the basic photo-electrochemical behavior of an AlGaN/GaN hetero-structure under monochromatic light to clarify the carrier-transfer process in PEC etching. Based on the photocarrier-regulated electrochemical (PREC) process, we succeeded in self-terminating and depth-controllable etching of an AlGaN/GaN hetero-structure with a very smooth surface. We also examined the electrical properties of a recessed-gate AlGaN/GaN HEMT fabricated using the PREC process.

II. METHODS

An I-Al\(_{0.25}\)Ga\(_{0.75}\)N/i-GaN hetero-structure grown on a Si substrate was used as the starting wafer, as shown in Fig. 1(a). The thickness of the AlGaN barrier layer was 25 nm. A Ti/Al/Ti/Au (20/50/20/50 nm) multilayer ohmic electrode was formed using the electron-beam evaporation method, followed by annealing at 830 °C for 1 min in a N\(_2\) ambient atmosphere. Then, a SiO\(_2\) film (100 nm) was formed by sputtering and lithography to define the etching region. We carried out PEC etching using a three-electrode cell (Fig. 1(b)), which consisted of the sample to be etched as the working electrode (WE), Pt counter electrode (CE), and Ag/AgCl reference electrode (RE), immersed in a mixture of 1 mol/L H\(_2\)SO\(_4\) and 1 mol/L H\(_3\)PO\(_4\) (pH = 1.7) as the electrolyte. The
ohmic contact was electrically connected to an outer circuit, in which the potential of WE was controlled with respect to the RE by using a potentiostat with a Princeton Applied Research VersaSTAT 4. Xenon (Xe) lamp as the light source, and monochromatic light passing through band-path filters was irradiated from the top of the WE.

Prior to etching experiments, the basic photo-electrochemical behavior of the AlGaN/GaN hetero-structure was investigated by measuring the current under monochromatic light. Considering the photo-electrochemical behavior with potential distribution, the relationships between electrochemical reactions and carrier-transfer processes were also investigated. Based on the regulation of carrier transfer, two types of PEC etching conditions were compared to investigate the effect of the carrier-transfer process on etching behavior. We then fabricated as Schottky diode and Schottky-gate HEMTs to evaluate the electrical properties of the AlGaN/GaN hetero-structure samples after etching.

III. RESULTS AND DISCUSSION

A. Photo-electrochemical properties of the AlGaN/GaN hetero-structure

Figure 2 shows the current-voltage ($J_{EC}$-$V_{EC}$) characteristics of the AlGaN/GaN hetero-structure immersed in the electrolyte in the dark (a black line) and under light of wavelengths ($\lambda$) of 300 (red line), 360 (blue line), and 400 nm (green line). Figure 3 shows the potential distribution of the electrolyte/AlGaN/GaN structure at voltages ($V_{EC}$) of (a) $-1.0$, (b) $1.0$, and (c) $3.0$ V, calculated using the one-dimensional Poisson equation.

Negative currents (reduction currents) were observed at potentials negative to around $0.8$ V under all light conditions. Since the surface potential of AlGaN becomes low by applying negative voltage, as shown in Fig. 3(a), the electrons in two-dimensional electron gas (2DEG) flow toward the electrolyte/AlGaN interface and cause reduction reactions. The electron concentration in 2DEG is generally as high as $10^{19}$ cm$^{-3}$, a reason why the reduction currents were barely affected by light illumination. At the positive voltage, few currents were observed on the AlGaN/GaN hetero-structure in
the dark and under light of $\lambda = 400\text{ nm}$ since electron flow was restricted due to the AlGaN barrier layer, and few holes existed in both AlGaN and GaN layers. Although light of $\lambda = 400\text{ nm}$ was absorbed in the Si substrate, photo-carriers could not flow due to the existence of the high-resistive buffer layer. This rectifying behavior observed on the AlGaN/GaN hetero-structure was similar to the n-type semiconductor electrodes in the dark, where positive current behaviors by photo-carriers are quite different. Under light of $\lambda = 360\text{ nm}$, which penetrates the AlGaN layer and is absorbed in the GaN layer, positive current (oxidation current) was observed with $V_{EC}$ larger than around 1.0 V. Considering the potential distribution at $V_{EC} = 1.0\text{ V}$ (Fig. 3(b)), electrons existed in 2DEG with high concentration, which prevented photo-holes from flowing toward the electrolyte/AlGaN interface; photo-carriers resulted in recombination and little current for surface oxidation was observed at $V_{EC} = 1.0\text{ V}$. The amount of electrons concentrated in 2DEG, however, decreased with an increase in $V_{EC}$ and fully depleted at $V_{EC} = 3.0\text{ V}$, as shown in Fig. 3(c). In such a situation, photo-holes can flow toward the electrolyte/AlGaN interface and cause oxidation reactions; thus, oxidation current increases rapidly at around $V_{EC} = 3.0\text{ V}$, as shown in Fig. 2. Obviously, oxidation reactions under this condition are caused by photo-holes generated in the GaN layer.

Under light of $\lambda = 300\text{ nm}$, which is absorbed in both AlGaN and GaN layers, oxidation current started to flow at around $V_{EC} = -0.5\text{ V}$, and subsequently saturated at around $V_{EC} = 0\text{ V}$. Since strong electric field exists in the AlGaN layer even at low $V_{EC}$, as shown in Fig. 3(b), photo-electrons and holes were transferred to the 2DEG and electrolyte/AlGaN interface, respectively; thus, oxidation current flowed even at relatively low $V_{EC}$. As previously discussed, photo-carriers generated in the GaN layer cannot flow at $V_{EC}$ below 1.0 V due to the existence of a high concentration of electrons in the 2DEG; thus, it is obvious that oxidation reactions under this condition are caused by photo-holes generated in the AlGaN layer.

Thus, we found that oxidation reactions on the AlGaN/GaN hetero-structure are caused by either photo-holes generated in the GaN layer or those generated in the AlGaN layer, or both. In addition, we can regulate the supply of photo-holes by selecting the appropriate $\lambda$ and $V_{EC}$. In Section III B, we discuss the importance of photo-hole regulation in achieving homogeneous PEC etching.

### B. PEC etching based on regulation of photo-carriers

We first carried out PEC etching by using photo-holes generated in the GaN layer. Figure 4(a) shows an atomic-force microscopy (AFM) image of a sample after PEC etching with $V_{EC} = 5.0\text{ V}$, $\lambda = 360\text{ nm}$, light intensity ($P_{IN}$) = 1.0 mW/cm$^2$, and etching time ($t_{EC}$) = 16 min. The SiO$_2$ film used for defining the etching region was completely removed by HF-based solution. From the AFM measurement, there was no difference in height between the masked and un-masked regions for etching despite large amounts of oxidation current. From the top scanning electron microscopy (SEM) image shown in Fig. 4(b), however, small pores with diameters of 20 nm or less could be observed only at an un-masked region, indicating that inhomogeneous etching occurred. Since the pore depth was estimated to be 240 nm from the cross-sectional SEM image shown in Fig. 4(c), pores seemed to pierce the AlGaN layer. The pore diameter in the AlGaN layer was estimated to be 20 nm or less as was the case with that estimated from the top SEM image, whereas the pore diameter in the GaN layer was estimated to be about 50 to 100 nm. An increase in $f_{EC}$ leads to an increase in the pore diameter in the GaN layer (pore diameter in the AlGaN layer remained unchanged), indicating that carrier transfer at the electrolyte/GaN interface occurred rather than at the electrolyte/AlGaN interface.

From the above results, we can conclude that photo-holes generated in the GaN layer cause inhomogeneous etching. Generally, inhomogeneous etching of a semiconductor results from localized carrier transfer by high-electric field applied at the electrolyte/semiconductor interface. Similar to the case with the carrier transfer in a Schottky barrier diode, localized carrier transfer may be caused by inhomogeneous potential distribution due to the concentration of the electric field on crystallographic disorders such as dislocations and vacancies. Since inhomogeneous etching observed under this condition will be the origin of rough surfaces, the use of photo-holes generated in the GaN layer is unfavorable for the fabrication of recessed-gate structures.

Next, we carried out PEC etching by using photo-holes generated in the AlGaN layer. Figure 5(a) shows an AFM image of a sample after PEC etching with $V_{EC} = -0.2\text{ V}$, $\lambda = 300\text{ nm}$, $P_{IN} = 1.0\text{ mW/cm}^2$, and $t_{EC} = 80\text{ min}$. The etched step could be clearly observed between the masked and un-masked regions, unlike the sample etched by photo-holes generated in the GaN layer. The root mean square of the etched region was only 0.41 nm. Figures 5(b) and 5(c) show the top and cross-sectional SEM images of the sample etched under this condition. There seem to be no pores and
no irregularity on the etched region, indicating that homogeneous etching can be achieved by using photo-holes generated in the AlGaN layer.

Figure 6(a) shows the relationship between the etching depth and $t_{EC}$ obtained on the sample etched with $P_{IN} = 0.5$ mW/cm$^2$. Etching depth increased linearly with $t_{EC}$ at the initial stage. However, we found that etching was terminated at a specific depth below the initial AlGaN-layer thickness of 25 nm. In other words, self-termination of etching was observed in the process of AlGaN-layer etching. Figure 6(b) shows the relationship between the self-termination depth and $P_{IN}$. We found that the self-termination depth increased linearly with $P_{IN}$, indicating that this phenomenon is related to the amount of photo-holes generated in the AlGaN layer.

From the above results, we considered a mechanism of PEC etching that involves photo-holes generated in the AlGaN layer. As previously mentioned, photo-holes generated in the AlGaN layer are transferred to the electrolyte/AlGaN interface by internal polarization. They cause oxidation reactions of AlGaN, and the resulting oxides, such as Ga$_2$O$_3$ and Al$_2$O$_3$, dissolve in the electrolyte, leading to etching of the AlGaN layer. Since photo-holes generated in the AlGaN layer decrease due to the thinning of the layer, etching reactions are suppressed at a specific AlGaN-layer thickness, which cannot generate sufficient photo-holes for surface oxidation; thus, a self-termination phenomenon occurred. Since the amount of photo-holes varies with $P_{IN}$, the self-terminating depth is a function of $P_{IN}$, as shown in Fig. 6(b).

Thus, the regulation of photo-holes critically affects etching behavior. Homogeneous etching can be achieved by preventing photo-holes generated in the GaN layer from participating in oxidation reactions. In addition, PEC etching involving photo-holes generated in the AlGaN layer is terminated spontaneously, and the self-termination depth can be controlled by $P_{IN}$. The self-termination phenomenon may help in suppressing unintentional variations in recess depth.

C. Electrical properties of recessed-gate AlGaN/GaN HEMTs formed using the PREC process

We evaluated the electrical properties of PREC-etched AlGaN/GaN hetero-structures by fabricating a Schottky diode and Schottky-gate HEMT. The AlGaN-layer thickness of the un-etched and PREC-etched regions is 25 and 8 nm, respectively.

Figure 7 shows the capacitance-voltage characteristics measured at 100 kHz for Schottky diodes fabricated on planar (black) and PREC-etched (red) samples: symbols represent experimental results, and solid lines represent theoretical curves, assuming an AlGaN-layer thickness of 25 nm for the planar sample and 8 nm for the PREC-etched sample. The $V_{th}$ and saturated capacitance value increased by thinning the AlGaN layer as expected. The experimental data were well reproduced by the theoretical curve, and no hysteresis was observed between the positive and negative sweep directions.
suggesting that a nearly ideal Schottky interface was formed on both samples.

Figures 8(a) and 8(b) show the drain current-voltage ($I_{DS}$–$V_{DS}$) characteristics of planar-gate and recessed-gate AlGaN/GaN Schottky HEMTs with a gate length ($L_G$) of 10 $\mu$m and source-drain spacing of 30 $\mu$m. Both samples showed good $I-V$ curves with constant saturation currents and pinch-off behavior. The transfer characteristics of the planar-gate and recessed-gate AlGaN/GaN Schottky HEMT in the saturated region ($V_{DS} = 10$ V) are compared in Fig. 9. The $V_{th}$ determined using the linear extrapolation method was $-2.42$ and $0.30$ V for the planar-gate and recessed-gate HEMTs, respectively, as shown in Fig. 9(a). The positive shift of $V_{th}$ could be confirmed with the recessed-gate structure, and no hysteresis was observed between positive and negative sweep directions. In addition, the $g_m$ of the recessed-gate HEMT was higher than that of the planar-gate HEMT due to the thinning of the AlGaN layer. In previous

FIG. 7. Capacitance-voltage characteristics measured at 100 kHz for the Schottky diode fabricated on planar (black) and PREC-etched (red) samples: symbols represent experimental results, and solid lines represent the theoretical curve assuming an AlGaN-layer thickness of 25 nm for the planar sample and 8 nm for the PREC-etched sample.

FIG. 8. Drain current-voltage ($I_{DS}$–$V_{DS}$) characteristics of (a) planar-gate and (b) recessed-gate AlGaN HEMTs with a gate length ($L_G$) of 10 $\mu$m and source-drain spacing of 30 $\mu$m.

FIG. 9. Transfer characteristics of planar-gate (black) and recessed-gate (red) Schottky HEMTs in the saturated region ($V_{DS} = 10$ V): (a) linear plots of $I_{DS}$ and transconductance ($g_m$), and (b) semi-log plots of $I_{DS}$ and gate current ($I_{GSL}$) as a function of gate voltage ($V_{GS}$).
reports on the recessed-gate HEMTs fabricated by dry etching, the positive $V_{th}$ was obtained as expected; however, the $g_m$ tended to be degraded due to the increase in gate current ($I_{GS}$). On the other hand, both positive $V_{th}$ and improvement in $g_m$ were simultaneously achieved in the recessed-gate HEMT fabricated by the present PREC etching since no significant increase in $I_{GS}$ was observed after PREC etching as shown in Fig. 9(b). These electrical characteristics suggest that no significant damage was induced in the AlGaN/GaN hetero-structures during PREC etching. We can conclude that PREC etching is desirable for fabricating recessed-gate structures.

### IV. CONCLUSIONS

We investigated the PREC etching process to fabricate recessed-gate AlGaN/GaN HEMTs without inducing damage. The basic photo-electrochemical characteristics of AlGaN/GaN hetero-structures revealed that two types of photo-holes generated in either the AlGaN layer or GaN layer could be supplied to the solid/liquid interface separately by selecting an appropriate light wavelength and voltage. The hole-regulation under these conditions significantly affected etching behavior: photo-holes generated in the GaN layer caused inhomogeneous etching and those generated in the AlGaN layer caused homogeneous etching. In the PREC process, the phenomenon of self-termination etching was also observed. The self-termination depth could be controlled by light intensity, enabling us to obtain the desired AlGaN-layer thickness without unintentional variation. The recessed-gate AlGaN/GaN HEMT showed positive threshold voltage and improvement in transconductance compared to the planar-gate AlGaN/GaN HEMT. The PREC process, therefore, is very attractive for use in fabricating recessed-gate structures.

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