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Synthesis of a Cauer Equivalent Circuit for Electric Devices from Computed and Measured Data

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Abstract—This paper proposes a method for modeling electric devices based on a Cauer circuit whose circuit parameters are directly determined from measured or computed data using the adjoint variable method. It has been shown that electric devices that are governed by the quasi-static Maxwell's equation can be modeled by the Cauer circuit. From the synthesized Cauer circuit, eddy current losses can be evaluated for a wide frequency range. Moreover, it can be embedded into circuit simulator to perform time-domain analysis. It is shown that the Cauer circuit whose parameters are identified using the proposed method works better for a simple numerical model than that whose parameters are identified using a genetic algorithm. Moreover, the Cauer equivalent circuit of a reactor and a power inductor is synthesized from the measured data using the proposed method. It is shown that the input impedance of the reactor and power inductor is well approximated by the Cauer circuit over the frequency domain of interest.

Index Terms—Adjoint variable method (AVM), Cauer equivalent circuit, Curve fitting, Litz wire, Sensitivity analysis.

I. INTRODUCTION

TO downsize electric devices such as transformers, electric motors, and inductors, the driving frequency has been increased, which has led to an increase in eddy current losses due to the skin and proximity effects. The finite element (FE) method can effectively compute the eddy currents, although its computational cost can be large because the device model has to be discretized into sufficiently fine elements that are smaller than the skin depth. In particular, repeated FE analysis in a design process can be unacceptably time-consuming.

To accelerate computations, equivalent circuit models that consider the eddy current effects have been proposed. For example, the Cauer equivalent circuit of a steel sheet has been derived from the analytical solution to the quasi-static Maxwell's equations [1]-[3]. The Foster equivalent circuit of an inductor with arbitrary geometry has been synthesized using model order reduction [4, 5] applied to the FE equation of the quasi-static Maxwell's equation. It has been shown that the Cauer equivalent circuit can be derived from the Foster circuit via a rational polynomial representation [6]. Of note, the Cauer equivalent circuit has been directly derived from the FE equation of the 2-D and 3-D quasi-static Maxwell's equations

[7, 8]. This means that a wide class of electric devices can be modeled using the Cauer circuit. In fact, it has been shown that permanent magnet synchronous motors and induction motors can be modeled by the Cauer circuit [9, 10]. Moreover, the electrochemical impedance of a diffusion and reaction process has been represented by the Cauer circuit [11]. With these approaches, the synthesized Cauer equivalent circuit can be embedded into circuit simulators and time-domain analysis can be readily performed.

One can also determine the circuit parameters of the Cauer circuit by curve fitting. The parameters are determined so that the error between the input impedance of the equivalent circuit and that of the electric device of interest is minimized. The identification of equivalent circuit parameters using stochastic approaches such as genetic algorithms (GAs) has been adopted because of their high search ability and versatility [12]-[16]. Moreover, the authors have successfully applied stochastic approach to the modeling of a wireless power transfer device [17], where the curve fitting was performed using a GA. One of the merits of the curve-fitting approach is that we only need the computed or measured input impedance of a device. This approach is especially effective when FE analysis is ineffective but measurements are available, as in the case when there is uncertainty in the material properties or extremely fine structures, such as litz wire, are considered. A stochastic method would fail to uniquely determine the circuit parameters because of its stochastic nature unless a suitably weighted regularization term is introduced to the cost function [18]. Stochastic approaches also have a relatively high computational cost.

Sensitivity analysis based on the adjoint technique, which is a deterministic approach, has been used for circuit design and analysis [19]-[26]. Tellegen's approach, which uses an adjoint network, is widely used for circuit design [19]-[21]. It is also possible to directly compute the sensitivity of circuit equations based on the adjoint variable method (AVM) [22]-[26]. In particular, the complex AVM has been proposed for analyzing complex linear systems such as RL circuits [25, 26]. Deterministic approaches are rather simple and have a smaller computational cost compared with that of stochastic approaches. As described above, there are many studies on the application of AVM to optimal design. However, there have been little studies to identify the circuit parameters using AVM. In particular, AVM has not been applied to identification of the Cauer circuit that has the above-mentioned engineering importance.

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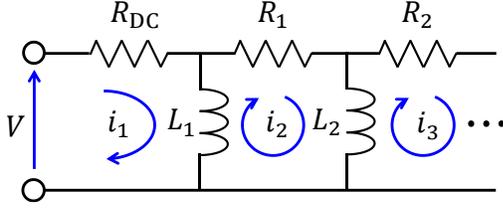


Fig. 1. Cauer equivalent circuit

In this paper, we propose a method for modeling electric devices based on a Cauer circuit whose circuit parameters are directly determined from either measured or computed data using AVM. To evaluate the effectiveness of the proposed method, we apply it to an FE model of a 20-turn inductor. Moreover, the proposed method is used to synthesize a Cauer circuit model from the measured data of a reactor and a power inductor.

II. CAUER EQUIVALENT CIRCUIT REPRESENTATION

Let us consider the Cauer equivalent circuit shown in Fig. 1, which can be derived from the quasi-static Maxwell's equations to effectively evaluate the eddy current loss and perform dynamic simulation. The quasi-static Maxwell's equations in the Laplace domain can be written as

$$\text{rot}v(\text{rot}\mathbf{A}) + j\omega\sigma(\mathbf{A} + \text{grad}\varphi) = \mathbf{J}, \quad (1a)$$

$$\text{div}\{j\omega\sigma(\mathbf{A} + \text{grad}\varphi)\} = 0, \quad (1b)$$

where \mathbf{A} , φ , v , j , ω , σ , and \mathbf{J} denote the vector and scalar potentials, magnetic reluctivity, imaginary unit, angular frequency, electric conductivity, and current density, respectively. By applying the weighted residual method in conjunction with the Galerkin method to (1), we obtain

$$\sum_j A_j \left[\int_{\Omega} \text{rot}\mathbf{N}_i \cdot v \text{rot}\mathbf{N}_j d\Omega + j\omega\sigma \int_{\Omega} \mathbf{N}_i \cdot \mathbf{N}_j d\Omega \right] \quad (2a)$$

$$+ j\omega\sigma \sum_k \varphi_k \int_{\Omega} \mathbf{N}_i \cdot \text{grad}\mathbf{N}_k d\Omega = I \int_{\Omega} \mathbf{N}_i \cdot \mathbf{j}_0 d\Omega,$$

$$j\omega\sigma \sum_j A_j \int_{\Omega} \mathbf{N}_j \cdot \text{grad}\mathbf{N}_u d\Omega \quad (2b)$$

$$+ j\omega\sigma \sum_k \varphi_k \int_{\Omega} \text{grad}\mathbf{N}_k \cdot \text{grad}\mathbf{N}_u d\Omega = 0,$$

where \mathbf{N}_i , N_i , I , and \mathbf{j}_0 denote the vector and scalar interpolation function, current, and unit current density, respectively. The electromagnetic field is assumed to be coupled, as expressed by the following circuit equation:

$$V = R_0 I + j\omega(L_0 I + \phi), \quad (3)$$

where R_0 , L_0 , V , and ϕ respectively denote the external resistance and inductance, input voltage, and magnetic flux, which is computed as $\phi = \sum_j A_j \int_{\Omega} \mathbf{N}_j \cdot \mathbf{j}_0 d\Omega$. We express (2) and (3) in matrix form as

$$\mathbf{K}\mathbf{z} + j\omega\mathbf{N}\mathbf{z} = \mathbf{V}\mathbf{b}, \quad (4a)$$

where \mathbf{K} , $\mathbf{N} \in \mathbb{R}^{m \times m}$, $\mathbf{z} \in \mathbb{C}^m$, $\mathbf{b} = [0, 0, \dots, 1]^t \in \mathbb{R}^m$, and m is the number of degrees of freedom in (2) and (3). In addition, the output current is expressed by

$$I = \mathbf{b}^t \mathbf{z}. \quad (4b)$$

To obtain the transfer function that corresponds to the admittance function, denoted here by $Y(\omega)$, model order reduction techniques can be applied to (4) [4, 5]. Consequently, $Y(\omega)$ is represented by a rational function of the form

$$Y(\omega) = \frac{\beta_0 + \beta_1(j\omega) + \beta_2(j\omega)^2 + \dots}{\alpha_0 + \alpha_1(j\omega) + \alpha_2(j\omega)^2 + \dots}. \quad (5)$$

By applying the Euclid algorithm to (5), a continued fraction of the form

$$Z(\omega) = \frac{1}{Y(\omega)} = R_{\text{DC}} + \frac{1}{\frac{1}{j\omega L_1} + \frac{1}{R_1 + \frac{1}{\frac{1}{j\omega L_2} + \dots}}}, \quad (6)$$

can be derived [5]. One can find that the continued fraction in (6) corresponds to the input impedance of the Cauer circuit shown in Fig. 1. The input-output relation of the system governed by (1) can thus be approximately represented by a Cauer circuit.

It is possible to assign a physical interpretation to the circuit parameters in the Cauer circuit. At sufficiently low frequencies, almost no current goes through R_1 ; most of it goes through L_1 because $j\omega L_1$ is sufficiently smaller than the impedance of the higher stages. This means that R_{DC} and L_1 correspond to the DC resistance and inductance, respectively, for the main flux without the eddy current effect. An increase in frequency gives rise to eddy current loss and a demagnetizing field due to eddy currents, which are represented by R_1 and L_2 , respectively. Similarly, the effects at higher frequencies are represented by R_k ($k \geq 2$) and L_k ($k \geq 3$).

The principal goal of this work is to model electric devices with a Cauer circuit via measurements, not field analysis. This method is particularly useful when FE modeling is difficult because of multiple spatial scales, as in the case for devices that include litz wires or soft magnetic composites and those with materials that have uncertain characteristics.

III. IDENTIFICATION METHOD FOR EQUIVALENT CIRCUIT PARAMETERS

To determine the circuit parameters $\mathbf{x} = [R_1, R_2, \dots, R_{P-1}, X_1, X_2, \dots, X_P]^t \in \mathbb{R}^{2P-1}$ of the Cauer circuit whose stage number is P , we solve the optimization problem defined by

$$\min_{\mathbf{x}} F(\mathbf{x}), \quad F(\mathbf{x}) = \sum_{q=1}^{N_S} |Z(\mathbf{x}, R_{DC}, \omega_q) - Z_S(\omega_q)|^2, \quad (7)$$

sub.to $R_k \geq 0, L_k \geq 0 \quad (k = 1, 2, \dots),$

where N_S , ω_q , $Z(\mathbf{x}, R_{DC}, \omega_q)$, and $Z_S(\omega_q)$ denote the number of sampling points, q -th angular frequency, impedance of the Cauer circuit, and measured (or computed) impedance, respectively. In addition, the reactance $X_k = \omega_0 L_k$ is defined with the maximum sampling frequency ω_0 . The circuit equation for the Cauer circuit is expressed as

$$Z(\omega)\mathbf{i} = \mathbf{v}, \quad (8a)$$

$$Z_{kl}(\omega) = \begin{cases} -j\frac{\omega}{\omega_0}X_{l-1} & (k = l-1) \\ R_{l-1} + j\frac{\omega}{\omega_0}(X_{l-1} + X_l) & (k = l) \\ -j\frac{\omega}{\omega_0}X_l & (k = l+1) \end{cases}, \quad (8b)$$

$$\mathbf{v} = [V, 0, 0, \dots, 0]^t, \quad (8c)$$

where V denotes the input voltage, and $R_{l-1} = R_{DC}$ when $k = l = 1$. Because it is difficult to directly evaluate the sensitivity $\partial F(\mathbf{x})/\partial x_n$ ($n = 1, 2, \dots, 2P-1$), we adopt the AVM in which the augmented objective function $\bar{F}(\mathbf{x})$ is minimized as follows:

$$\min_{\mathbf{x}} \bar{F}(\mathbf{x}), \quad \bar{F}(\mathbf{x}) = F(\mathbf{x}) + \sum_{q=1}^{N_S} \Phi_q^t (Z(\omega_q)\mathbf{i}_q - \mathbf{v}), \quad (9)$$

sub.to $R_k \geq 0, L_k \geq 0 \quad (k = 1, 2, \dots),$

where $\Phi_q \in \mathbb{C}^P$ denotes the adjoint variable, which corresponds to the Lagrange multiplier in the context of nonlinear programming. Note that $\bar{F}(\mathbf{x}) \approx F(\mathbf{x})$ provided that \mathbf{i}_q is a good approximation of the solution to (8a). The derivative of $\bar{F}(\mathbf{x})$ with respect to the n -th circuit parameter x_n is given by

$$\frac{\partial \bar{F}(\mathbf{x})}{\partial x_n} = \sum_{q=1}^{N_S} \left[\Phi_q^t \frac{\partial Z(\omega_q)}{\partial x_n} \mathbf{i}_q + \left\{ Z(\omega_q)\Phi_q + \frac{\partial F(\mathbf{x})}{\partial \mathbf{i}_q} \right\}^t \frac{\partial \mathbf{i}_q}{\partial x_n} \right], \quad (10)$$

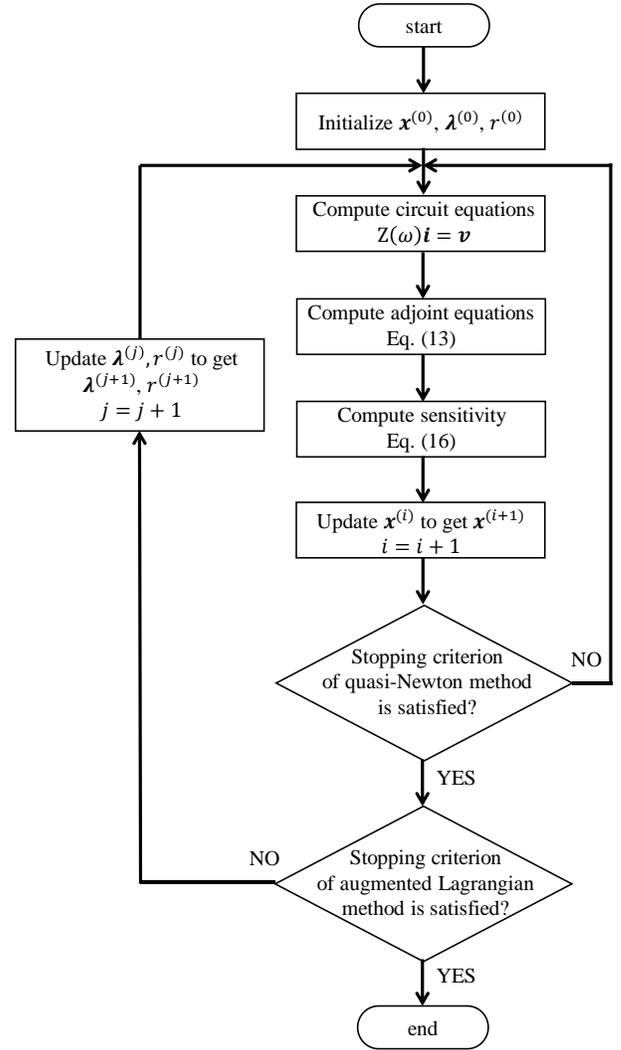


Fig. 2. Flow diagram of the proposed method

where it is assumed that the current \mathbf{i}_q is an implicit function of x_n . It is difficult to directly evaluate the second term in (10). To eliminate it, we solve the adjoint equations

$$Z(\omega_q)\Phi_q = -\frac{\partial F(\mathbf{x})}{\partial \mathbf{i}_q}, \quad q = 1, 2, \dots, N_S, \quad (11)$$

to obtain the adjoint variable Φ_q . By substituting Φ_q into the first term in (10), we can obtain the derivative of $\bar{F}(\mathbf{x})$. Here, the right-hand side in (11) needs to be carefully treated because there it is not guaranteed that $F(\mathbf{x})$ is a holomorphic function that satisfies the Cauchy-Riemann equations

$$\frac{\partial F^r(\mathbf{x})}{\partial \mathbf{i}_q^r} = \frac{\partial F^i(\mathbf{x})}{\partial \mathbf{i}_q^i}, \quad (12a)$$

$$\frac{\partial F^r(\mathbf{x})}{\partial \mathbf{i}_q^i} = -\frac{\partial F^i(\mathbf{x})}{\partial \mathbf{i}_q^r}, \quad (12b)$$

where the superscripts r and i denote the real and imaginary parts, respectively. For this reason, we apply the complex AVM [25, 26] so that (11) is modified to

$$Z(\omega_q)\Phi_q = \frac{\partial F(\mathbf{x})}{\partial \mathbf{i}_q^r} - j \frac{\partial F(\mathbf{x})}{\partial \mathbf{i}_q^i}, \quad (13)$$

where the right-hand side in (13) is computed only with respect to the circuit current of the first stage i_{1q} because the input impedance of the Cauer circuit can be evaluated as $Z(\mathbf{x}, R_{DC}, \omega_q) = V/i_{1q}$. The derivative of $\bar{F}(\mathbf{x})$ is now given by [26]

$$\frac{\partial \bar{F}(\mathbf{x})}{\partial x_n} = -\text{Re} \left(\sum_{q=1}^{N_s} \mathbf{i}_q^t \frac{\partial Z(\omega_q)}{\partial x_n} \Phi_q \right). \quad (14)$$

In addition, we impose a constraint on $\bar{F}(\mathbf{x})$ so that the resultant circuit parameters take non-negative values. To do so, the constraint functions $g_k(\mathbf{x}), k = 1, 2, \dots$ are introduced on the basis of the augmented Lagrangian method [27] as follows:

$$\min_{\mathbf{x}} M(\mathbf{x}, \boldsymbol{\lambda}; r),$$

$$M(\mathbf{x}, \boldsymbol{\lambda}; r) = F(\mathbf{x}) + \sum_{q=1}^{N_s} \Phi_q^t (Z(\omega_q) \mathbf{i}_q - \mathbf{v}) + \frac{1}{4r} \sum_k [\{\min(0, 2r g_k(\mathbf{x}) + \lambda_k)\}^2 - (\lambda_k)^2], \quad (15)$$

where r and $\boldsymbol{\lambda}$ denote the penalty coefficient and Lagrangian multiplier, respectively. The derivative of $M(\mathbf{x}, \boldsymbol{\lambda}; r)$ is given by

$$\frac{\partial M(\mathbf{x}, \boldsymbol{\lambda}; r)}{\partial x_n} = -\text{Re} \left(\sum_{q=1}^{N_s} \mathbf{i}_q^t \frac{\partial Z(\omega_q)}{\partial x_n} \Phi_q \right) + \frac{1}{2r} \sum_k \left[\min(0, 2r g_k(\mathbf{x}) + \lambda_k) \min \left(0, 2r \frac{\partial g_k(\mathbf{x})}{\partial x_n} \right) \right] \quad (16)$$

To update the circuit parameters using (16), the quasi-Newton method based on the Broyden–Fletcher–Goldfarb–Shanno algorithm is employed in this work. The above process is schematically shown in Fig. 2.

IV. NUMERICAL RESULTS

A. Frequency Characteristics and Circuit Parameters

Although the proposed method can be applied to various electric devices, we confine ourselves to inductor and reactor models for the verification. Let us first consider the simple inductor model shown in Fig. 3 whose input impedance is

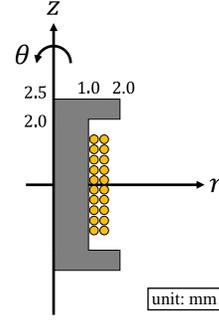


Fig. 3. 20-turn inductor model (strand radius: 0.15 mm, relative permeability of the magnetic core: 1000, conductivity of the strand: 5.76×10^7 S/m)

TABLE I
OPTIMIZATION PARAMETERS FOR PROPOSED METHOD

Initial circuit parameter $x_n^{(0)}$	1.0 Ω
Initial multiplier value $\lambda_n^{(0)}$	0.0
Initial penalty parameter $r^{(0)}$	5.0
Stopping criterion of the quasi-Newton method	$\left \frac{\partial M(\mathbf{x}, \boldsymbol{\lambda}, r)}{\partial \mathbf{x}} \right < 10^{-8}$
Stopping criterion of the augmented Lagrangian method	$ \min\{g(\mathbf{x}), -\lambda/2r\} < 10^{-6}$

TABLE II
OPTIMIZATION PARAMETERS FOR GA

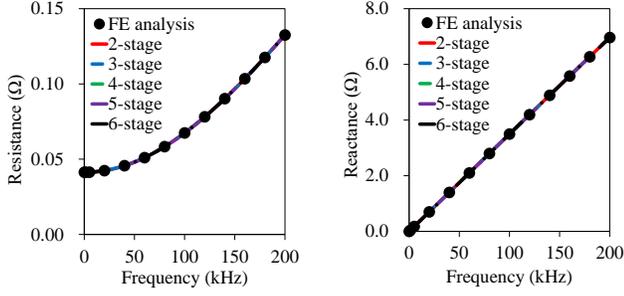
Number of generations	10,000
Number of populations ¹	$50 \times (2P - 1)$

¹Note that P denotes the number of stages of the Cauer circuit shown in Fig. 1.

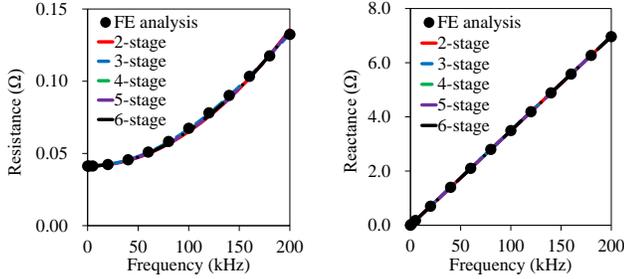
analyzed using the FE method. We compare the performance of the proposed method with that of conventional circuit parameter identification using the GA [12]–[17]. The parameters for the proposed method and the GA are summarized in TABLE I and TABLE II, respectively. The results are plotted in Fig. 4. The input resistance and reactance values computed from the equivalent circuits whose parameters were determined by the two approaches agree well with those computed from FE analysis over the frequency range of interest. Because the skin depth, about 0.15 mm at the highest frequency (200 kHz), is larger than the strand radius, the proximity effect is stronger than the skin effect.

The corresponding fitting errors for both methods are plotted in Fig. 5. It can be concluded that the proposed method has smaller fitting errors in comparison with GA for this model.

The convergence histories are shown in Fig. 6. The circuit parameters after convergence for the proposed method and the GA are summarized in TABLE III and TABLE IV, respectively. The value of R_{DC} is pre-computed from the geometry of the model shown in Fig. 3 without curve fitting. We found that the squared error for the proposed method shown in Fig. 6(a) decreases to about 5.0×10^{-10} when the number of circuit stages, P , is greater than 4. In contrast, the squared error for the



(a) Proposed method



(b) GA

Fig. 4. Frequency characteristics of equivalent circuits obtained with proposed method and GA

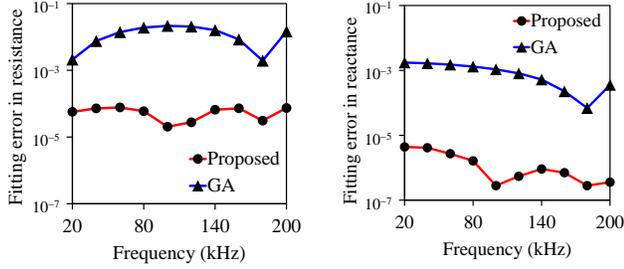
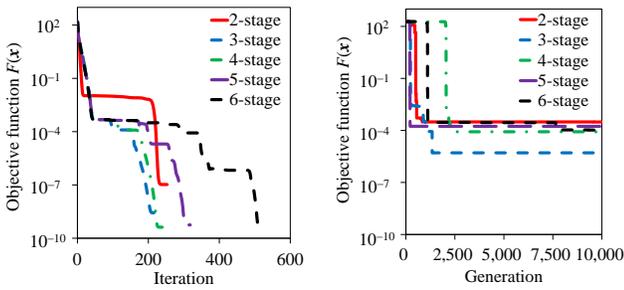


Fig. 5. Fitting errors with 4-stage circuit



(a) Proposed method

(b) GA

Fig. 6. Convergence histories for proposed method and GA

GA shown in Fig. 6(b) tends to stagnate at around 10^{-4} regardless of P , even though the evolution process was continued for over 10,000 generations. In addition, the synthesized circuit parameters determined from the GA include extraordinarily large values (e.g., $R_4 = 1.3 \times 10^{137} \Omega$) due to the stochastic nature of the algorithm.

 TABLE III
 CIRCUIT PARAMETERS DETERMINED FROM PROPOSED METHOD

	Number of circuit stages				
	2	3	4	5	6
$R_{DC} (\Omega)$	4.13×10^{-2}	4.13×10^{-2}	4.13×10^{-2}	4.13×10^{-2}	4.13×10^{-2}
$R_1 (\Omega)$	4.49×10^2	4.45×10^2	4.45×10^2	4.46×10^2	4.46×10^2
$R_2 (\Omega)$		6.20×10^3	6.77×10^3	3.14×10^3	9.01×10^3
$R_3 (\Omega)$			1.23×10^3	6.02×10^3	1.03×10^4
$R_4 (\Omega)$				2.23×10^4	1.45×10^1
$R_5 (\Omega)$					6.78×10^{-4}
$L_1 (H)$	5.57×10^{-6}	5.57×10^{-6}	5.57×10^{-6}	5.57×10^{-6}	5.57×10^{-6}
$L_2 (H)$	1.53×10^{-4}	1.55×10^{-4}	1.54×10^{-4}	1.54×10^{-4}	1.53×10^{-4}
$L_3 (H)$		2.93×10^{-3}	6.61×10^{-2}	9.48×10^{-3}	4.02×10^{-4}
$L_4 (H)$			2.18×10^{-3}	2.72×10^{-5}	3.82×10^{-3}
$L_5 (H)$				6.45×10^{-7}	4.32×10^{-6}
$L_6 (H)$					7.44×10^{-5}

 TABLE IV
 CIRCUIT PARAMETERS DETERMINED FROM GA

	Number of circuit stages				
	2	3	4	5	6
$R_{DC} (\Omega)$	4.13×10^{-2}	4.13×10^{-2}	4.13×10^{-2}	4.13×10^{-2}	4.13×10^{-2}
$R_1 (\Omega)$	5.08×10^2	4.60×10^2	4.85×10^2	4.96×10^2	4.91×10^2
$R_2 (\Omega)$		2.61×10^0	4.48×10^3	2.04×10^3	8.34×10^4
$R_3 (\Omega)$			1.64×10^{18}	8.3×10^{133}	9.28×10^3
$R_4 (\Omega)$				1.3×10^{137}	9.06×10^4
$R_5 (\Omega)$					2.8×10^{150}
$L_1 (H)$	5.56×10^{-6}	5.57×10^{-6}	5.56×10^{-6}	5.56×10^{-6}	5.56×10^{-6}
$L_2 (H)$	4.45×10^{-5}	1.41×10^{-4}	1.01×10^{-4}	7.48×10^{-5}	9.35×10^{-5}
$L_3 (H)$		7.61×10^{-1}	2.8×10^{-15}	7.5×10^{-17}	7.1×10^{-12}
$L_4 (H)$			6.12×10^0	1.9×10^{117}	9.24×10^{-7}
$L_5 (H)$				8.1×10^{119}	4.0×10^{-12}
$L_6 (H)$					4.6×10^{140}

B. Dependence of Settings on Convergence

In order to study the dependence of the convergence of the proposed method and GA on the initial guess and hyper parameters, we perform the circuit identification under different settings. The convergence histories for 4-stage circuit are plotted for the five cases in Figs. 7 and 8. We can see that the squared errors for the proposed method shown in Fig. 7 decrease to about 5.0×10^{-10} regardless of the initial circuit parameter. In contrast, the squared error for GA shown in Fig. 8 tends to stagnate at around 10^{-4} even when the random seed and number of populations, which is chosen 30 times larger than the number of unknowns at least, are changed.

C. High-Frequency Characteristics of Equivalent Circuit

The Cauer circuit model was evaluated at a frequency that is above the highest frequency (200 kHz) considered for the curve fitting. The frequency characteristics of the input impedance of the Cauer circuit up to 1 MHz are plotted in Fig. 9, where the circuit parameters were identified using the proposed method and the GA. It can be seen that the Cauer circuit works well

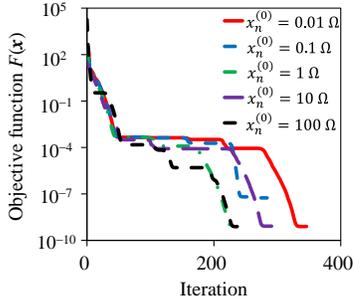
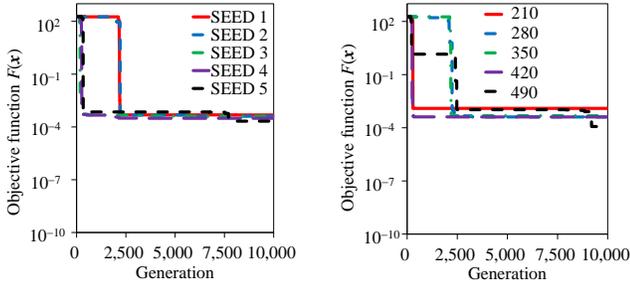
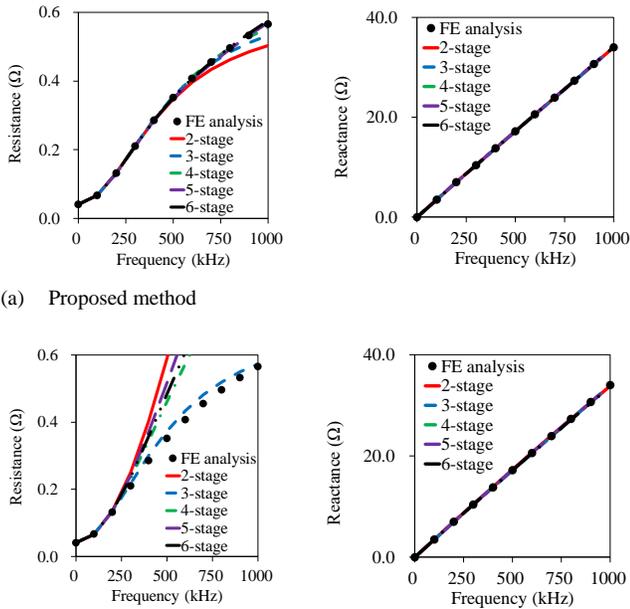


Fig. 7. Dependence of initial parameter on convergence for proposed method



(a) Random seed (b) Number of populations

Fig. 8. Dependence of hyper parameters on convergence of GA. Convergence for different number of populations is shown in (b).



(b) GA

Fig. 9. High-frequency characteristics of equivalent circuits obtained with proposed method and GA

over the frequency range with a rather small stage number when the proposed method is applied. In contrast, the GA requires a greater number of stages for the identified Cauer circuit. The results demonstrate the superiority of the proposed method for the considered problem.



(a) Reactor with 10-turn litz wire (b) Power inductor [28]

Fig. 10. Reactor and power inductor for experimental validation

Number of strands	12
Strand radius	0.16 mm
Twist pitch	20 mm
Magnetic core type	PQ 50/50
Magnetic core material	PC47
Air gap in magnetic core	0.3 mm

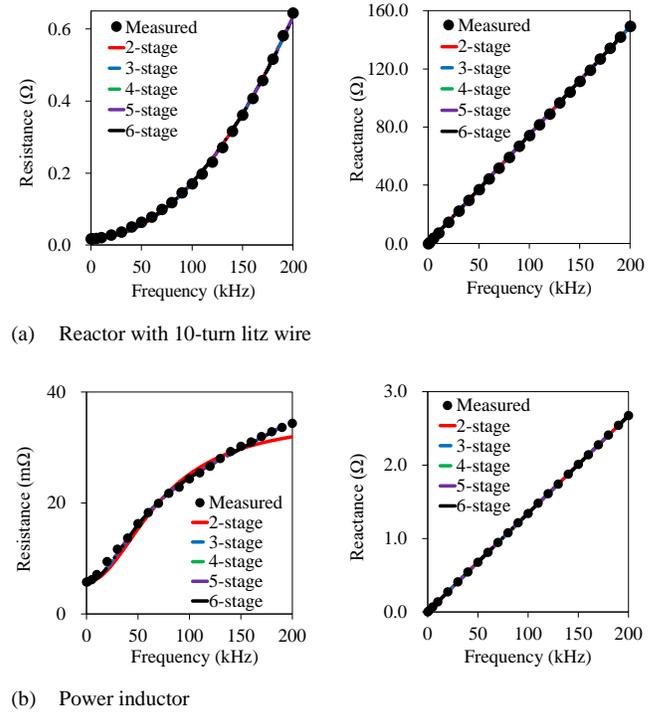


Fig. 11. Frequency dependence of reactor and power inductor

V. EXPERIMENTAL VALIDATION AND SIMULATED TIME RESPONSE

A. Circuit Identification

The proposed method was used to identify the Cauer circuit parameters from the measured input impedance of the reactor with 10-turn litz wire and power inductor used in, e.g. a DC-DC converter [28] shown in Fig. 10. TABLE V summarizes specifications of the reactor. The frequency characteristics of the input impedance are plotted in Fig. 11.

TABLE VI
IDENTIFIED CIRCUIT PARAMETERS FOR REACTOR
WITH 10-TURN LITZ WIRE

	Number of circuit stages				
	2	3	4	5	6
R_{DC} (Ω)	1.85×10^{-2}	1.85×10^{-2}	1.85×10^{-2}	1.85×10^{-2}	1.85×10^{-2}
R_1 (Ω)	3.61×10^4	3.61×10^4	3.61×10^4	3.61×10^4	2.50×10^{-3}
R_2 (Ω)		2.27×10^1	1.16×10^{-5}	6.41×10^4	3.63×10^4
R_3 (Ω)			1.87×10^2	5.48×10^3	1.86×10^4
R_4 (Ω)				7.43×10^2	3.02×10^3
R_5 (Ω)					2.14×10^3
L_1 (H)	1.19×10^{-4}	1.19×10^{-4}	1.19×10^{-4}	1.19×10^{-4}	7.30×10^{-3}
L_2 (H)	-1.6×10^{-13}	8.62×10^{-3}	2.97×10^{-2}	-1.6×10^{-13}	1.21×10^{-4}
L_3 (H)		-1.6×10^{-13}	-1.6×10^{-13}	7.08×10^{-3}	-1.6×10^{-13}
L_4 (H)			8.10×10^{-3}	1.40×10^{-3}	1.58×10^{-4}
L_5 (H)				7.68×10^{-4}	3.97×10^{-3}
L_6 (H)					1.03×10^{-3}

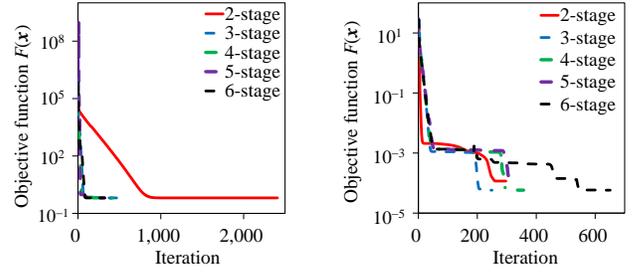
TABLE VII
IDENTIFIED CIRCUIT PARAMETERS FOR POWER INDUCTOR

	Number of circuit stages				
	2	3	4	5	6
R_{DC} (Ω)	5.73×10^{-3}	5.73×10^{-3}	5.73×10^{-3}	5.73×10^{-3}	5.73×10^{-3}
R_1 (Ω)	3.32×10^1	2.58×10^1	2.56×10^1	2.57×10^1	2.57×10^1
R_2 (Ω)		7.70×10^2	7.68×10^2	7.69×10^2	7.15×10^2
R_3 (Ω)			5.62×10^{-3}	2.05×10^{-2}	5.48×10^1
R_4 (Ω)				4.03×10^{-1}	1.96×10^0
R_5 (Ω)					2.25×10^0
L_1 (H)	2.18×10^{-6}	2.19×10^{-6}	2.19×10^{-6}	2.19×10^{-6}	2.19×10^{-6}
L_2 (H)	7.10×10^{-5}	6.99×10^{-5}	6.99×10^{-5}	6.99×10^{-5}	7.00×10^{-5}
L_3 (H)		4.95×10^{-9}	4.50×10^{-5}	2.70×10^{-5}	1.56×10^{-4}
L_4 (H)			3.91×10^{-5}	1.06×10^{-5}	2.26×10^{-5}
L_5 (H)				1.82×10^{-5}	1.21×10^{-16}
L_6 (H)					1.61×10^{-7}

The AC resistance and reactance were measured with an LCR meter (HIOKI IM3523), where the signal level for the measurements was set to 10 (mA) to exclude the magnetic core losses. The resistance and reactance computed from the identified Cauer circuit parameters agree well with those obtained from the measurements. The resultant circuit parameters are summarized in TABLES VI, VII and the convergence histories are shown in Fig. 12. The converged values in Fig. 12(a) plateau at about 1.0 whereas those in Figs. 6(a) and 12(b) continue to decrease. This might be due to the measurement errors that cannot be well approximated by the Cauer circuit.

B. Computation of Time Response

As an application of the proposed method, we computed the time evolution of the eddy current loss in the 10-turn reactor using the identified Cauer circuit parameters. The test circuit 1 with a sinusoidal input voltage is shown in Fig. 13 where Z_p denotes the parasitic impedance of C and R_{LOAD} . The time response was computed with the software LTspice® and



(a) Reactor with 10-turn litz wire (b) Power inductor

Fig. 12. Convergence history for reactor and power inductor

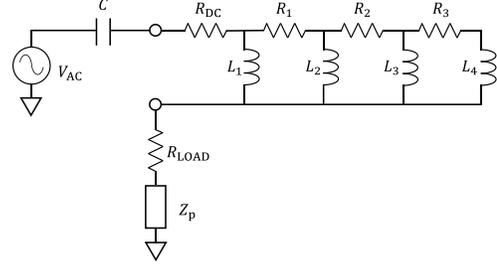


Fig. 13. Test circuit 1 for transient analysis

($V_{AC} = -11$ to 11 V, Frequency = 183 kHz, $C = 6$ nF,
 $R_{LOAD} = 5 \Omega$, $Z_p = 1.4 + j1.6 \Omega$)

measured with a power analyzer (HIOKI PW6001). We take the absolute value of L_3 , negative value close to zero, in the simulation to stabilize the circuit behavior. The transient responses of the 10-turn reactor and load resistance are plotted in Fig. 14. TABLE VIII summarizes the simulated and measured active powers of R_{LOAD} . The loss in the proposed circuit shown in Fig. 14(b) is mainly contributed from R_{DC} and R_1 . It is found that the relative error between the active powers obtained from the proposed circuit and measurement is less than 5 % while the relative error for the conventional circuit is greater than 10 %, in which the eddy current loss is not considered.

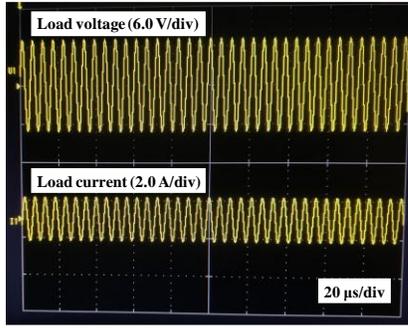
In order to apply the proposed circuit to practical electric devices such as a DC-DC converter, we consider here the test circuit 2 with a pulsed input voltage as shown in Fig. 15. The transient losses of the reactor are plotted in Fig. 16. It can be seen that the loss evaluated using the proposed circuit is greater than that of the simple circuit due to the eddy current loss.

C. Discussion

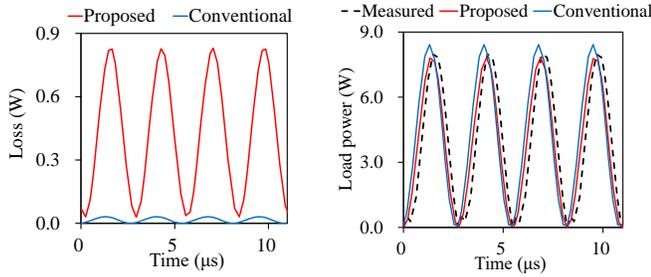
The losses due to eddy currents and circulating current in the multi-turn litz wire coils have to be considered at high frequencies. Numerical methods have been proposed to evaluate these effects considering the bundling and twisting structures in the winding coils [29]-[32]. An experimental method for extracting the AC resistance in a transformer with litz wire has been proposed, where the geometric tolerance in the litz wire is directly considered [33]. This experimental method is more practical because accurate information on internal components in electric devices is often unavailable.

TABLE VIII
 SIMULATED AND MEASURED ACTIVE POWERS OF R_{LOAD}

Method	Active Power (W)
Measured	3.76 W
Proposed	3.91 W
Conventional	4.21 W



(a) Measured results of load voltage and load current



(b) Loss of 10-turn reactor

(c) Load power

Fig. 14. Transient responses for test circuit 1

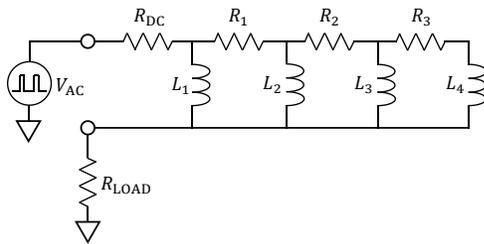


Fig. 15. Test circuit 2 for transient analysis

($V_{AC} = -10$ to 10 V, period = $10 \mu\text{s}$, duty = 50 %, $R_{LOAD} = 5.0 \Omega$)

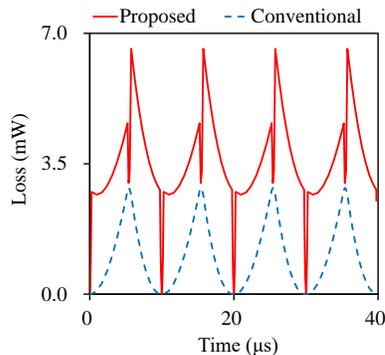


Fig. 16. Transient losses of 10-turn reactor in test circuit 2

For such cases, even if the internal device structure is unknown, the circuit model with eddy current effects, which can be readily embedded into circuit simulators, can be constructed using the proposed method.

VI. CONCLUSION

In this paper, we proposed a method for modeling electric devices using the Cauer equivalent circuit considering the eddy current losses. The circuit parameters are directly determined from measured data using AVM. The proposed method was demonstrated to outperform a conventional method using a GA for the FE model of a simple inductor. Moreover, the proposed method was shown to be valid for the identification of Cauer circuit parameters from the measured input impedance of a reactor with litz wire and a power inductor.

The validity of the proposed method for the modeling of windings in electric motors and generators should be verified in future studies. Moreover, the validity for devices with saturable magnetic cores should be examined.

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REFERENCES

- [1] T. Miyazaki, T. Mifune, T. Matsuo, Y. Shindo, Y. Takahashi, and K. Fujiwara, "Equivalent circuit modeling of dynamic hysteretic property of silicon steel under pulse width modulation excitation," *J. Appl. Phys.*, 117, 17D110, 2015.
- [2] Y. Shindo and O. Noro, "Simple Circuit Simulation Models for Eddy Current in Magnetic Sheets and Wires," *IEEJ Trans. FM*, vol. 134, no. 4, pp. 173-181, 2014.
- [3] E. J. Tarasiewicz, A. S. Morched, A. Narang, and E. P. Dick, "Frequency Dependent Eddy Current Models for Nonlinear Iron Cores," *IEEE Trans. Power Systems*, vol. 8, no. 2, pp. 588-597, 1993.
- [4] Y. Sato and H. Igarashi, "Generation of Equivalent Circuit from Finite-Element Model Using Model Order Reduction," *IEEE Trans. Magn.*, vol. 52, no. 3, Art. no. 1100304, 2016.
- [5] Y. Sato, T. Shimotani, and H. Igarashi, "Synthesis of Cauer-Equivalent Circuit Based on Model Order Reduction Considering Nonlinear Magnetic Property," *IEEE Trans. Magn.*, vol. 53, no. 6, Art. no. 1100204, 2017.
- [6] T. Shimotani, Y. Sato, and H. Igarashi, "Direct synthesis of equivalent circuits from reduced FE models using proper orthogonal decomposition," *COMPEL*, vol. 35, no. 6, pp. 2035-2044, 2016.
- [7] S. Hiruma and H. Igarashi, "Model Order Reduction for Linear Time-Invariant System with Symmetric Positive-Definite Matrices: Synthesis of Cauer-Equivalent Circuit," *IEEE Trans. Magn.*, vol. 56, no. 3, Art. no. 7400608, 2020.
- [8] A. Kameari, H. Ebrahimi, K. Sugahara, Y. Shindo, and T. Matsuo, "Cauer Ladder Network Representation of Eddy-Current Fields for Model Order Reduction Using Finite-Element Method," *IEEE Trans. Magn.*, vol. 54, no. 3, Art. no. 7201804, 2018.
- [9] T. Matsuo, K. Sugahara, A. Kameari, and Y. Shindo, "Model Order Reduction of an Induction Motor Using a Cauer Ladder Network," *IEEE Trans. Magn.*, vol. 56, no. 3, Art. no. 7514704, 2020.
- [10] N. Minowa, Y. Takahashi, and K. Fujiwara, "Iron Loss Analysis of Interior Permanent Magnet Synchronous Motors Using Dynamic

- Hysteresis Model Represented by Cauer Circuit," *IEEE Trans. Magn.*, vol. 55, no. 6, Art. no. 7401404, 2019.
- [11] A. A. Moya, "A Ladder Network Modeling the Electrochemical Impedance of the Diffusion and Reaction Processes in Semi-Infinite Space," *Phys. Chem. Chem. Phys.*, vol. 18, no. 5, pp. 3812-3816, 2016.
- [12] P. Mukherjee and L. Satish, "Construction of Equivalent Circuit of a Single and Isolated Transformer Winding from FRA Data Using the ABC Algorithm," *IEEE Trans. Power Del.*, vol. 27, no. 2, pp. 963-970, 2012.
- [13] N. V. Korovkin, V. L. Chechurin, and M. Hayakawa, "Inverse Problems in Electric Circuits and Electromagnetics," US: Springer, Chapter 5, pp. 275-285, 2007.
- [14] M. M. Shabestary, A. J. Ghanizadeh, G. B. Gharehpetian, and M. Agha-Mirsalim, "Ladder Network Parameters Determination Considering Nondominant Resonances of the Transformer Winding," *IEEE Trans. Power Del.*, vol. 29, no. 1, pp. 108-117, 2014.
- [15] A. Shintemirov, W. H. Tang, and Q. H. Wu, "Transformer Core Parameter Identification Using Frequency Response Analysis," *IEEE Trans. Magn.*, vol. 46, no. 1, pp. 141-149, 2010.
- [16] V. Rasthchi, E. Rahimpour, and E. M. Rezapour, "Using a genetic algorithm for parameter identification of transformer R-L-C-M model," *Elect. Eng.*, vol. 88, no. 5, pp. 417-422, 2006.
- [17] Y. Otomo, Y. Sato, S. Fujita, and H. Igarashi, "Synthesis of Equivalent Circuit of Wireless Power Transfer Device Using Homogenization-Based FEM," *IEEE Trans. Magn.*, vol. 54, no. 3, Art. no. 7401005, 2018.
- [18] T. Shimotani, H. Igarashi, E. Hashimoto, and H. Imanari, "Equivalent Circuit Allowing Loss Separation Synthesized from Field Computations: Application to Induction Heating," *IEEE Trans. Magn.*, vol. 56, no. 2, Art. no. 7505405, 2020.
- [19] S. W. Director and R. A. Rohrer, "The Generalized Adjoint Network and Network Sensitivities," *IEEE Trans. Circuit Theory*, vol. 16, no. 3, pp. 318-323, 1969.
- [20] S. W. Director and R. A. Rohrer, "Automated Network Design—The Frequency—Domain Case," *IEEE Trans. Circuit Theory*, vol. 16, no. 3, pp. 330-337, 1969.
- [21] P. Penfield, R. Spence, and S. Duinker, "A Generalized Form of Tellegen's Theorem," *IEEE Trans. Circuit Theory*, vol. 17, no. 3, pp. 302-305, 1970.
- [22] V. A. Monaco and P. Tiberio, "Computer-Aided Analysis of Microwave Circuits," *IEEE Trans. Microwave Theory Tech.*, vol. 22, no. 3, pp. 249-263, 1974.
- [23] Z. Ren, H. Qu, and X. Xu, "Computation of Second Order Capacitance Sensitivity Using Adjoint Method in Finite Element Modeling," *IEEE Trans. Magn.*, vol. 48, no. 2, pp. 231-234, 2012.
- [24] S. Barmada, A. Musolino, R. Rizzo, and M. Tucci, "Multi-Resolution Based Sensitivity Analysis of Complex Non-Linear Circuits," *IET Circuits Devices Syst.*, vol. 6, no. 3, pp. 176-186, 2012.
- [25] N. K. Nikolova, J. W. Bandler, and M. H. Bakr, "Adjoint Techniques for Sensitivity Analysis in High-Frequency Structure CAD," *IEEE Trans. Microwave Theory Tech.*, vol. 52, no. 1, pp. 403-419, 2004.
- [26] H. Igarashi and K. Watanabe, "Complex Adjoint Variable Method for Finite-Element Analysis of Eddy Current Problems," *IEEE Trans. Magn.*, vol. 46, no. 8, pp. 2739-2742, 2010.
- [27] J. Nocedal and S. J. Wright, "Numerical Optimization," US: Springer, Chapter 17, pp. 497-528, 2006.
- [28] Murata Manufacturing Co., Ltd., "FDA1055-H-2R2M," Accessed: Jun. 14, 2020. [Online]. Available: <https://www.murata.com/global/products/productdetail?partno=FDA1055-H-2R2M%23>
- [29] A. Roßkopf, E. Bär, C. Joffe, and C. Bonse, "Calculation of Power Losses in Litz Wire Systems by Coupling FEM and PEEC Method," *IEEE Trans. Power Electron.*, vol. 31, no. 9, pp. 6442-6449, 2016.
- [30] S. Hiruma, Y. Otomo, and H. Igarashi, "Eddy Current Analysis of Litz Wire Using Homogenization-Based FEM in Conjunction with Integral Equation," *IEEE Trans. Magn.*, vol. 54, no. 3, Art. no. 7001404, 2018.
- [31] S. Gyimóthy, S. Kaya, D. Obara, M. Shimada, M. Masuda, S. Bilicz, J. Pávó, and G. Varga "Loss Computation Method for Litz Cables with Emphasis on Bundle-Level Skin Effect," *IEEE Trans. Magn.*, vol. 55, no. 6, Art. no. 6300304, 2019.
- [32] T. Guillod, J. Huber, F. Krismer, and J. W. Kolar, "Litz Wire Losses: Effects of Twisting Imperfections," *Proc. of the IEEE Control and Modeling for Power Electronics Conf. (COMPEL)*, pp. 1-8, 2017.
- [33] K. Niyomsatian, J. Gyselinck, and R. V. Sabariego, "Experimental Extraction of Winding Resistance in Litz-Wire Transformers – Influence

of Winding Mutual Resistance," *IEEE Trans. Power Electron.*, vol. 34, no. 7, pp. 6736-6746, 2019.



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